Max. Marks:100

Seventh Semester B.E. Degree Examination, Dec.2018/Jan.2019 Embedded Computing Systems

Time: 3 hrs.

Note: Answer FIVE full questions, selecting atleast TWO questions from each part.

		PART - A	
1	a. b.	Explain the hardware and software architecture for the moving map display.	(06 Marks) (06 Marks)
	c.	Write the sequence diagram for transmitting a control input in model train controlled	er. (08 Marks)
2	a.	What is the difference section in the rain in the	(06 Marks)
	b.	Explain the basic in the programming me are	(06 Marks)
	c.	Write the UML collaboration diagram for the data compressor. Explain Huffman	coding for
		text compression.	(08 Marks)
		To de la Financia de la Chesarith a DMA controllar	(06 Morks)
3	a.	Definite a bab. Emplanii wang	(06 Marks) (06 Marks)
	b.	With a near sketch explain the meeting e-games	(00 Marks)
	c.		(08 Marks)
		III) Key board IV) Display.	(00 11111110)
4	a.	Discuss models of program in design and analysis.	(06 Marks)
4	b.	Explain program optimization techniques.	(08 Marks)
	c.		(06 Marks)
		Zipimi program	
		PART – B	
5	a.	Explain the architecture of RTOS with suitable example.	(08 Marks)
	b.	Discuss process, threads and CPU metrics.	(06 Marks)
	c.	Briefly explain rate monotonic scheduling and earliest - Deadline first scheduling.	(06 Marks)
6	a.	Explain interprocess communication mechanisms.	(10 Marks)
	b.	Discuss the theory of operation and requirements in telephone answering machine.	(10 Marks)
			(10 Marks)
7	6	With a neat sketch, explain OSI model for network.	(08 Marks)
7	a. b.	Explain distributed embedded architecture with a neat diagram.	(06 Marks)
	c.	Explain:	. 100
	٠.	i) Internet applications	
		ii) Internet security	
		iii) Sensor networks.	(06 Marks)
8		Write short notes on the following: CMRIT LIBRARY	
	a.	IDE BANGALORE - 560 037	
	b.	Simulator and debugger	- 1
	c.	BMW 850i brake and stability control	(20 Marks)
	d.	Cache memory.	(20 Mains)

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