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		CISCS SCRIEME			
USN			17CS44		
	N.	Fourth Semester B.E. Degree Examination, Jun	e/July 2019		
	**************************************	Microprocessors and Microcontro			
101					
Tip	ne: (3 hrs.	Max. Marks: 100		
	N	Note: Answer any FIVE full questions, choosing ONE full question	from each module.		
1	a. b.		croprocessor. (08 Marks)		
	c,	(i) Memory segmentation (ii) Flag Register	(06 Marks) = 4000H, DS = 2000H,		
		(i) MOV AL, [BX] (ii) MOV CL, [BP] (iii) MOV ES	: AX, [BX + 05] (06 Marks)		
OR					
2	a.	777 1 1 1 1 1 C 77711			
	b.	What is stack? Explain the working of PUSH and POP instructions	(08 Marks) (06 Marks)		
	c.				
		(i) assume (ii) org (iii) db (iv) equ	(06 Marks)		
		Module-2			
3	a.	screen, sets the cursor at the centre of screen and display the m	essage "Journey Towards		
	h	Excellence". Explain shift and rotate instructions of 8086.	(08 Marks) (06 Marks)		
	b. с.		· ·		
4		What is an interrupt vector table? Explain the steps a 8086 will ta	uke when it responds to an		
4	a.	interrupt.	(08 Marks)		
	b.	With example explain the following instructions of 8086.	•		
		(i) MUL (ii) DAA (iii) CWD (iv) STD	(06 Marks)		
	c.	Write a program to find the value of $x^2 + 2x + 5$, where x is 8 bit in	(06 Marks)		
שק	_	Module-3 What is deta integrity? Explain the methods used for data integrit	win Ram and ROM Alao		
5	a.	What is data integrity? Explain the methods used for data integrit find the checksum byte for 34H, 54H, 7FH, 11H, E6H and 99H.	(08 Marks)		

- Explain how signed numbers are represented in 8086. Also explain the significance of b. (06 Marks) overflow flag.
- Explain IN and OUT instructions. Show the design of an output port with an I/O address of (06 Marks) 99H using 74LS373.

		OR			
6	a. Differentiate between memory mapped I/O and I/O mapped I/O. Explain the cor				
		format of 8255.	(08 Marks)		
	b.	With example explain any five string manipulation instructions of 8086.	(06 Marks)		
	·c.	Write a program to find average of n different temperatures.			
	Module-4				
7	a.	Differentiate between RISC and CISC.	(06 Marks)		
	b.	b. With a neat block diagram explain ARM core data flow model. (06 Ma			
	c.	Explain the different operating modes of Arm. Also explain the complete ARM register set.			
			(08 Marks)		
		OR OR			
8	a.				
	b.	With the help of bit layout diagram explain current program status register of ARM.			
	_ •	The late of the second of the	(06 Marks)		
	c.	Explain the concepts of core Extensions and Pipeline in ARM processor.	(08 Marks)		
		No. of the second secon			
9		Module-5 With avanual avalais MOV and MVN instructions of ARM	70 C M M N N		
J	a. b.	With example explain MOV and MVN instructions of ARM.	(06 Marks)		
		Explain the different barrel shifter operations.	(06 Marks)		
	c.	Explain the arithmetic instructions of ARM.	(08 Marks)		
		OR BANGALORE 60 037			
10	a.	Explain multiply, branch and load store instructions of ARM.	(10 Manda)		
10	а. b.	With example explain SWAP instruction of ARM.	(10 Marks)		
	c.	Write ARM assembly language program to add two 32 bit numbers.	(04 Marks)		
	U.	write rules assembly language program to add two 32 on numbers.	(06 Marks)		