

Time: 3 hrs.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

1 a. What is data communication? With a neat diagram, explain the four basic topology.

(06 Marks)

b. With the help of a diagram, explain the functionalities of each layer of OSI reference model.
(10 Marks)

OR

2 a. What is the difference between a port address, a logical address and a physical address.

(06 Marks)

- b. What is line coding? Represent the sequence "01001110" using NRZ-L, NRL-I and Manchester scheme. (06 Marks)
- c. Explain digital signal transmission methods.

(04 Marks)

Module-2

- 3 a. Explain the PCM technique used for analog to digital conversion. (08 Marks)
 - b. Explain Amplitude Shift Keying (ASK) and Phase Shift Keying (PSK) modulation process.
 (06 Marks)
 - c. An analog signal carrier 4 bits per signal element. If 1000 signal elements are sent per second, find the bit rate. (02 Marks)

OR

4 a. What is TDM? Explain in detail.

(08 Marks)

b. Explain circuit switched network with an example and also briefly discuss the phases.

(04 Marks)

c. Explain in brief frequency hopping spread spectrum technique.

(04 Marks)

Module-3

- 5 a. How does data word and codeword represented in block coding and also explain how can error be detected and corrected by using block coding. (10 Marks)
 - b. Given data word 1001 and the divisor 1011:
 - i) Show the generator of the codeword at the sender site
 - ii) Show the checking of codeword at the receiver site (assume no error).

(06 Marks)

OR

- 6 a. With a neat diagram, explain Go-Back-N Automatic Repeat Request protocol of noisy channel and explain how flow control and error control is achieved. (10 Marks)
 - b. Explain the frame format of HDLC protocol.

(06 Marks)

Module-4

A/CA). (04 Marks)
(04 Marks)
(08 Marks)
(04 Marks)
(10 Marks)
(06 Marks)
1
(08 Marks)
(08 Marks)
1