Date: 16/04/2018

Time: 9.30am to 12.30 pm

CMRIT LIBRARY BANGALORE - 560 037

## ONE TIME EXIT SCHEME

	T	T	Т				Г
CNI							
)3N							

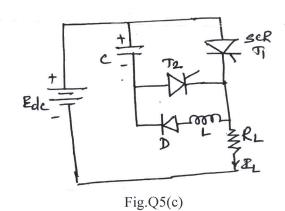
## Seventh Semester B.E. Degree Examination, April 2018 Power Electronics

## Q. No. 5 c.

For the Auxiliary communication circuit shown below Fig.Q5(c), compute the value of commutation capacitor 'C' and commutating inductor 'R' for the following data:

 $E_{dc}$  = 50 V,  $I_{L(max)}$  = 50 A,  $t_{off}$  of  $SCR_1$  = 30  $\mu s$ , chopping frequency f = 500 Hz and the load voltage variation required is 10% to 100%. Assume 50% tolerance on turn-off time of  $SCR_1$ .

(08 Marks)



CMRIT LIBRARY BANGALORE - 560 037