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Fourth Semester B.E. Degree Examination, June/July 2016
Computer Organization

Time: 3 hrs.

Max. Marks:100

**Note: Answer FIVE full questions, selecting
at least TWO questions from each part.**

PART – A

- 1 a. List the steps needed to execute the machine instruction given below in terms of transfers between the components of processor, memory and some control commands. ADD LOCA, R0. Assume the instruction is stored in memory location 'INSTR'. (05 Marks)
- b. Perform the following operations on the 5-bit signed numbers using 2's complement representation system. Further indicate whether overflow has occurred.
i) (-10) + (-13) ii) (-10) – (+4) iii) (+7) – (-15) iv) (+8) + (+10). (10 Marks)
- c. Write the basic performance equation. Explain the role of each of the parameters in the equation on the performance of the computer. (05 Marks)
- 2 a. Define addressing mode and explain any four addressing modes with an example for each. (09 Marks)
- b. What is subroutine linkage? With examples explain different ways of passing parameters to subroutine. (06 Marks)
- c. Explain in detail encoding of machine instructions into 32-bit words. Assume that there are 16 registers in the processor. (05 Marks)
- 3 a. Define interrupt. What are the overheads incurred in handling interrupts? (04 Marks)
- b. With neat sketches explain a method for handling interrupts from multiple devices. (10 Marks)
- c. What is bus arbitration? With a neat diagram, explain any one approach used for bus arbitration. (06 Marks)
- 4 a. With a block diagram, explain a general 8-bit parallel interface. (10 Marks)
- b. List out widely used bus standards in a computer system. Further, explain the READ operation using PCI bus with a timing diagram. (10 Marks)

PART – B

- 5 a. What is a cache? Explain any two cache mapping functions with neat sketches. (10 Marks)
- b. What is a virtual memory? With a neat diagram, explain the method for translating virtual address to physical address. (10 Marks)
- 6 a. Explain the design of a 4-bit carry-look ahead adder. (08 Marks)
- b. Multiply (+14) and (-6) using Booth's algorithm. (07 Marks)
- c. Perform the division of number 8 by 3 ($8 \div 3$) using restoring division method. (05 Marks)
- 7 a. Write the sequence of control steps to execute the instruction, ADD (R₃), R₁ on single bus architecture. (07 Marks)
- b. With a block diagram, describe the organization of a micro programmed control unit. (10 Marks)
- c. Bring out the differences between micro programmed control and Hard-Wired control. (03 Marks)
- 8 a. Explain any two shared memory multiprocessor models. (10 Marks)
- b. Briefly explain SISD and SIMD architectures with neat diagrams. (10 Marks)