



- 7 a. Explain with example, interrupt driven data transfer. (05 Marks)  
b. List and explain various registers involved in keyboard and display interfaces. (08 Marks)  
c. Explain working of synchronous bus with timing diagram for an input transfer operation. (07 Marks)
- 8 a. Explain concept of cache memory and explain direct mapping used to map cache to main memory. (10 Marks)  
b. What do you mean by virtual memory? Explain how translation of virtual address to physical address is being done. (10 Marks)

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