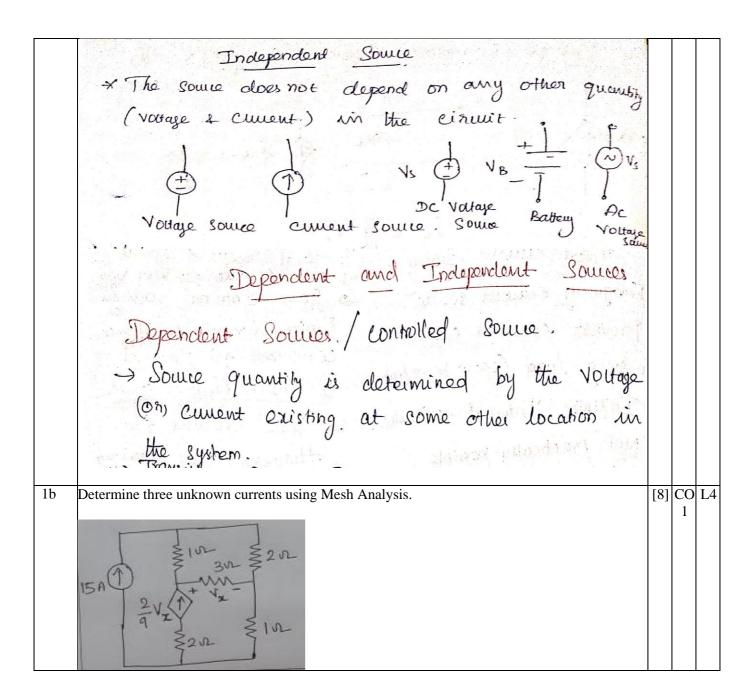
## CMR INSTITUTE OF TECHNOLOGY

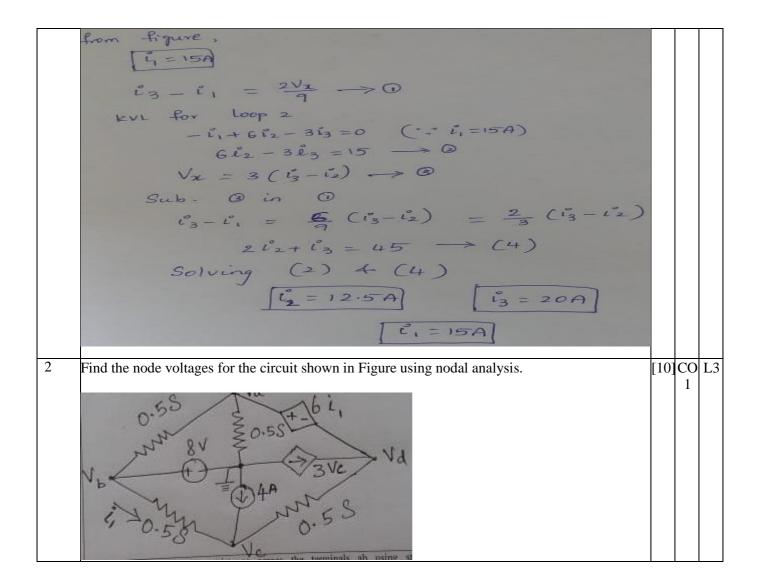
## **IAT 1 Solution**

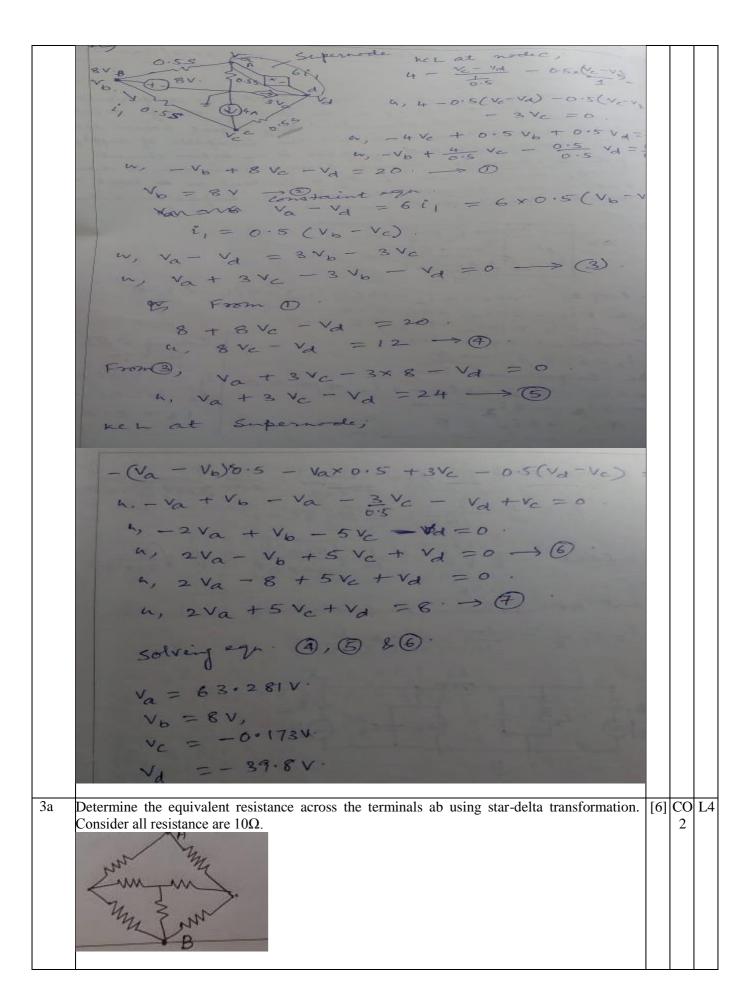


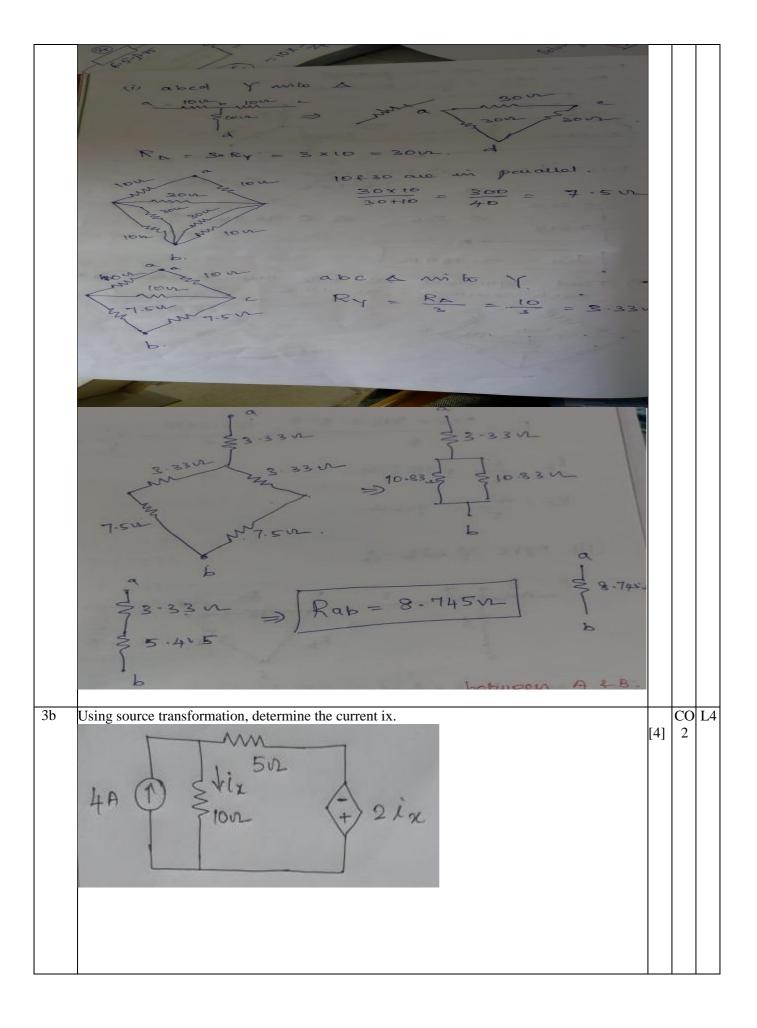
## Internal Assesment Test - I

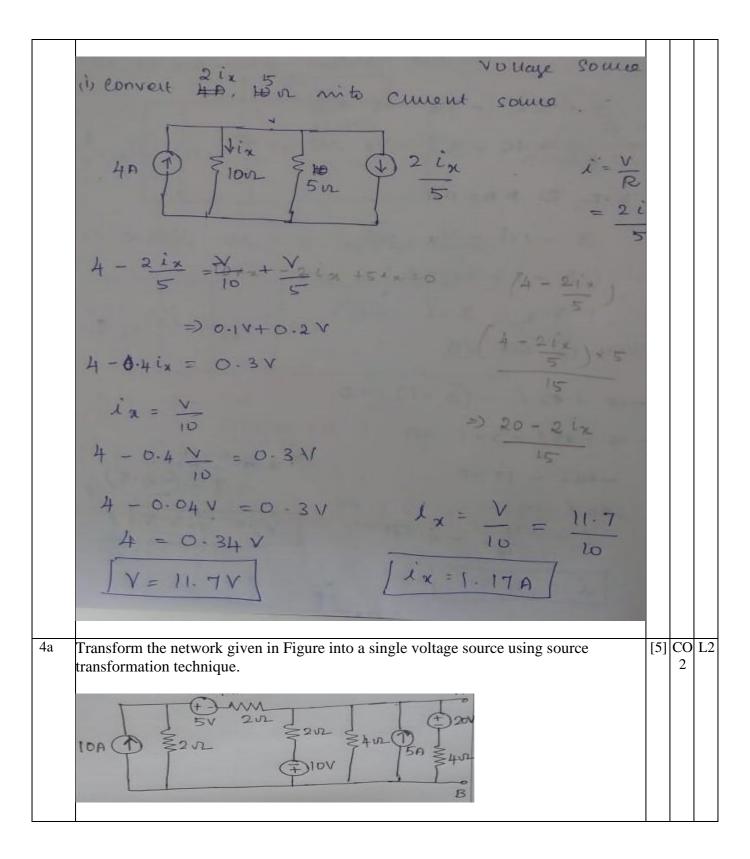
Sub:	Electric Circuit Analysis					Code: 18EE32				
Date: 06/09/2019 Duration: 90 mins				Max Marks:	50 Se	n: 3 - B	Branch: EEE			
1a Dis	stinguish between Edad Sour  Tragin  Which  Circuit  They  They  They  They  Not  Not	nouy e provide regar don!t mipossible l resiste source availab tically e sov	practical solution.  Consider of the area of the area.  Source of the area.	sources, (ii) act  I source  I source  To Load  To Intern  Cony  build a  not lt  wi the  sible cr	ive and passed is all Remarks	ive element cuuch istanco resist with	t to feine ( - zero	M [2]	CO 1	R B T
	→ A1	ailable	2 m	Mauk	wh.	Moura	10 ·			

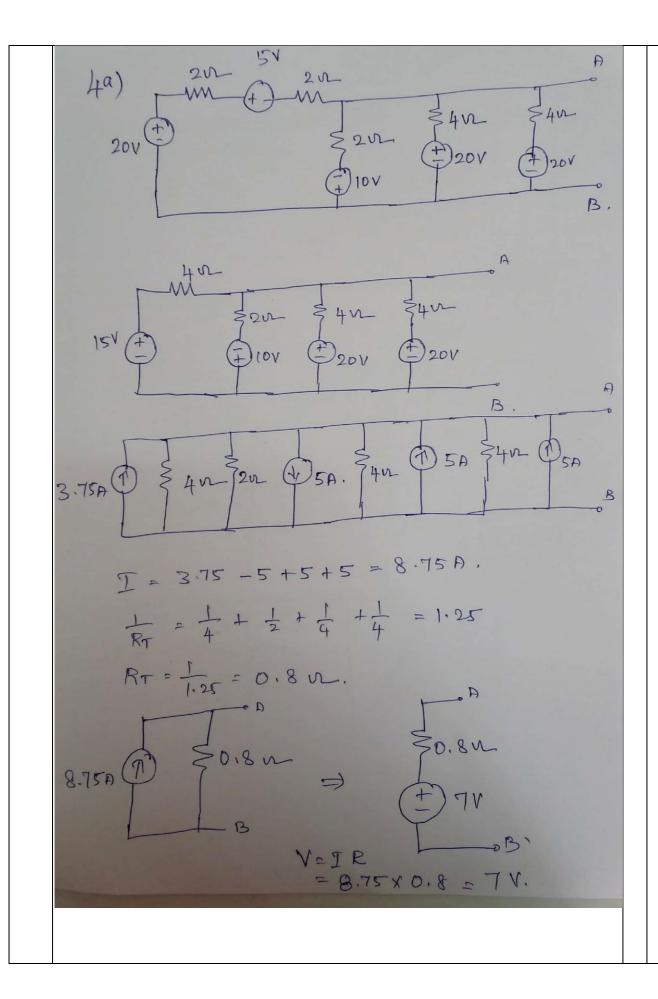


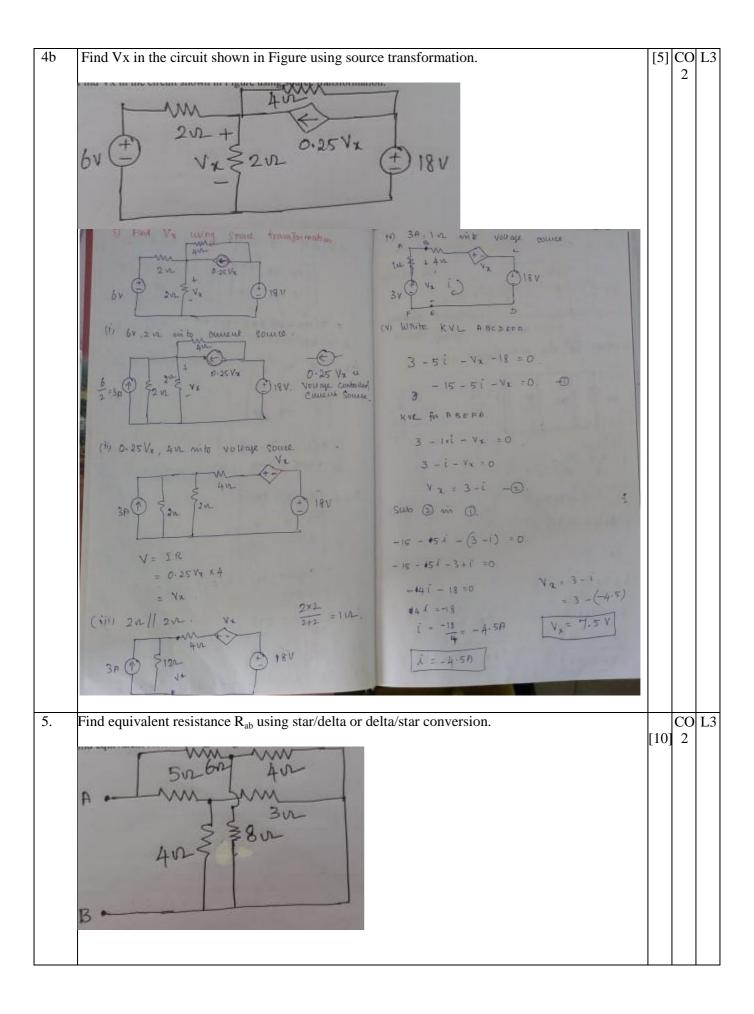


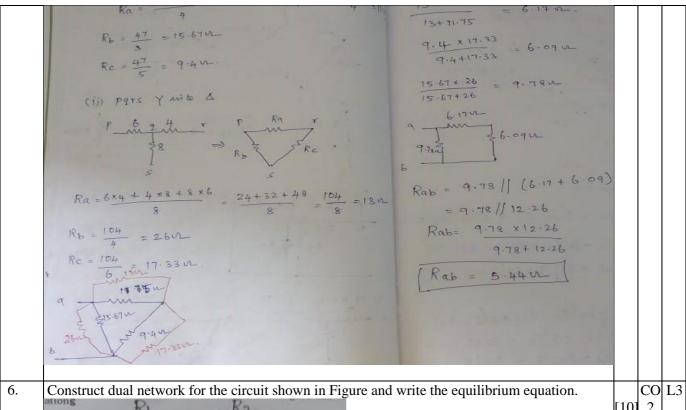


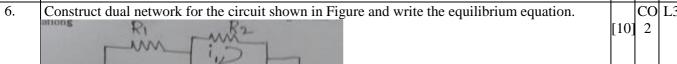


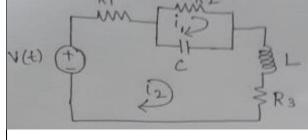


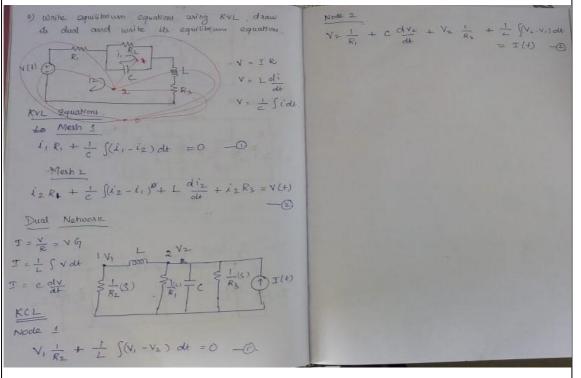












7. Find current ia using superposition theorem.

