


CMR INSTITUTE OF TECHNOLOGY		USN	<table border="1"> <tr> <td> </td><td> </td><td> </td><td> </td><td> </td><td> </td><td> </td><td> </td><td> </td> </tr> </table>																	
Internal Assessment Test – II Answer Key																				
Subject : System Software								Code : 16MCA25												
Date : 10.05.2017	Duration : 90 mins	Max Marks : 50	Sem : II	Branch : MCA																
<p align="center"><b>Answer Any FIVE FULL Questions</b></p>								Marks	OBE											
									CO	RBT										
1(a)	<p><b>What are the Basic Functions of Loader?</b></p> <p>A loader is a system program that performs the loading function. The most fundamental function of loader is to bring object program into memory and starts its execution.</p> <p>1) Design of Absolute Loader:</p> <p>The operation of absolute loader is very simple. The object code is loaded to specified locations in the memory. At the end the loader jumps to the specified address to begin execution of the loaded program. The role of absolute loader is simple and efficient. But the disadvantages are, the need for programmer to specify the actual address, and, difficult to use subroutine libraries.</p> <p><b>Begin</b>  read Header record  verify program name and length  read first Text record  <b>while</b> record type is &lt;&gt; 'E' <b>do</b>  <b>begin</b>  {if object code is in character form, convert into internal representation}  move object code to specified location in memory  read next object program record  <b>end</b>  jump to address specified in End record  <b>end</b></p> <p>2) Simple Bootstrap Loader</p> <p>When a computer is first turned on or restarted, a special type of absolute loader, called bootstrap loader is executed. This bootstrap loads the first program to be run by the computer -- usually an operating system. The bootstrap itself begins at address 0. It loads the OS starting address 0x80. No header record or control information, the object code is consecutive bytes of memory.</p>							[4]	CO3	L1										

<p>(b)</p>	<p><b>Explain relocation with the Bit Mask.</b></p> <p>If a machine primarily uses direct addressing and has a fixed instruction format, it is often more efficient to specify relocation using relocation bit</p> <p>Each instruction is associated with one relocation bit. It Indicates that the corresponding word should be modified or not.</p> <p>0: no modification is needed</p> <p>1: modification is needed</p> <p>This is specified in the columns 10-12 of text record (T), the format of text record, along with relocation bits is as follows.</p> <p>Text record:</p> <p>col 1: T</p> <p>col 2-7: starting address</p> <p>col 8-9: length (byte)</p> <p>col 10-12: relocation bits</p> <p>col 13-72: object code</p> <p>These relocation bits in a Text record are gathered into bit masks.</p> <p>Twelve-bit mask is used in each Text record (col:10-12 – relocation bits), since each text record contains less than 12 words, unused words are set to 0, and, any value that is to be modified during relocation must coincide with one of these 3-byte segments.</p> <p>E.g. FFC=111111111100</p> <p>E00=111000000000</p> <p>000000 00107A</p> <p>TΛ000000Λ1EΛFFCΛ140033Λ481039Λ000036Λ280030Λ300015Λ...Λ3C0003</p> <p>TΛ00001EΛ15ΛE00Λ0C0036Λ481061Λ080033Λ4C0000Λ...Λ000003Λ000000</p> <p>TΛ001039Λ1EΛFFCΛ040030Λ000030Λ...Λ30103FΛD8105DΛ280030Λ...</p> <p>TΛ001057Λ0AΛ 800Λ100036Λ4C0000ΛF1Λ001000</p> <p>TΛ001061Λ19ΛFE0Λ040030ΛE01079Λ...Λ508039ΛDC1079Λ2C0036Λ...</p> <p>EΛ000000</p>	<p>[6]</p>	<p>CO3</p>	<p>L2</p>
<p>2</p>	<p><b>Write short note on</b></p> <p><b>i) Automatic Library Search</b></p> <p><b>ii) Loader options</b></p>	<p>[10]</p>	<p>CO3</p>	<p>L1</p>

### Automatic Library Search

This feature allows a programmer to use standard subroutines without explicitly including them in the program to be loaded.

The routines are automatically retrieved from a library as they are needed during linking.

This allows programmer to use subroutines from one or more libraries. The subroutines called by the program being loaded are automatically fetched from the library, linked with the main program and loaded.

The loader searches the library or libraries specified for routines that contain the definitions of these symbols in the main program.

### Loader Options

Loader options allow the user to specify options that modify the standard processing. The options may be specified in three different ways. They are, specified using a command language, specified as a part of job control language that is processed by the operating system, and can be specified using loader control statements in the source program. Here are some examples of how options can be specified.

INCLUDE program-name (library-name) - read the designated object program from a library  
DELETE csect-name - delete the named control section from the set of programs being loaded  
CHANGE name1, name2 - external symbol name1 to be changed to name2 wherever it appears in the object programs

LIBRARY MYLIB - search MYLIB library before standard libraries  
NOCALL STDDEV, PLOT, CORREL - no loading and linking of unneeded routines  
Here is one more example giving, how commands can be specified as a part of object file, and the respective changes are carried out by the loader.

LIBRARY UTLIB

INCLUDE READ (UTLIB)

INCLUDE WRITE (UTLIB)

DELETE RDREC, WRREC

CHANGE RDREC, READ

CHANGE WRREC, WRITE

	<p>NOCALL SQRT, PLOT</p> <p>The commands are, use UTLIB ( say utility library), include READ and WRITE control sections from the library, delete the control sections RDREC and WRREC from the load, the change command causes all external references to the symbol RDREC to be changed to the symbol READ, similarly references to WRREC is changed to WRITE, finally, no call to the functions SQRT, PLOT, if they are used in the program.</p>			
3	<p><b>Write the Algorithm for pass-1 and pass-2 of Linking Loader</b></p> <pre> Pass 1: begin get PROGADDR from operating system set CSADDR to PROGADDR {for first control section} while not end of input do begin read next input record {Header record for control section} set CSLTH to control section length search ESTAB for control section name if found then set error flag {duplicate external symbol} else enter control section name into ESTAB with value CSADDR while record type () 'E' do begin read next input record if record type = 'D' then for each symbol in the record do begin search ESTAB for symbol name if found then set error flag {duplicate external symbol} else enter symbol into ESTAB with value (CSADDR + indicated address) end {for} end {while () 'E'} add CSLTH to CSADDR {starting address for next control section} end {while not EOF} end {Pass 1} </pre>	[10]	CO3	L2

	<p><b>Pass 2:</b></p> <pre> begin set CSADDR to PROGADDR set EXECADDR to PROGADDR while not end of input do begin read next input record {Header record} set CSLTH to control section length while record type () 'E' do begin read next input record if record type = 'T' then begin {if object code is in character form, convert into internal representation} move object code from record to location {CSADDR + specified address} end {if 'T'} else if record type = 'M' then begin search ESTAB for modifying symbol name if found then add or subtract symbol value at location {CSADDR + specified address} else set error flag {undefined external symbol} end {if 'M'} end {while () 'E'} if an address is specified {in End record} then set EXECADDR to {CSADDR + specified address} add CSLTH to CSADDR end {while not EOF} jump to location given by EXECADDR {to start execution of loaded program} end {Pass 2} </pre>																							
4(a)	<p><b>Discuss Macro Definition and Expansion with suitable Example</b></p> <p><b>Macro Definition and Expansion:</b> The figure shows the MACRO expansion. The left block shows the MACRO definition and the right block shows the expanded macro replacing the MACRO call with its block of executable instruction.</p> <p>M1 is a macro with two parameters D1 and D2. The MACRO stores the contents of register A in D1 and the contents of register B in D2. Later M1 is invoked with the parameters DATA1 and DATA2, Second time with DATA4 and DATA3. Every call of MACRO is expanded with the executable statements.</p> <table border="1" data-bbox="277 1362 1159 1829"> <thead> <tr> <th>Source</th> <th>Expanded source</th> </tr> </thead> <tbody> <tr> <td>M1 MACRO &amp;D1, &amp;D2</td> <td>.</td> </tr> <tr> <td>STA &amp;D1</td> <td>.</td> </tr> <tr> <td>STB &amp;D2</td> <td>.</td> </tr> <tr> <td>MEND</td> <td>{ STA DATA1</td> </tr> <tr> <td>.</td> <td>STB DATA2</td> </tr> <tr> <td>M1 DATA1, DATA2</td> <td>.</td> </tr> <tr> <td>.</td> <td>{ STA DATA4</td> </tr> <tr> <td>M1 DATA4, DATA3</td> <td>STB DATA3</td> </tr> <tr> <td></td> <td>.</td> </tr> </tbody> </table>	Source	Expanded source	M1 MACRO &D1, &D2	.	STA &D1	.	STB &D2	.	MEND	{ STA DATA1	.	STB DATA2	M1 DATA1, DATA2	.	.	{ STA DATA4	M1 DATA4, DATA3	STB DATA3		.	[5]	CO3	L1
Source	Expanded source																							
M1 MACRO &D1, &D2	.																							
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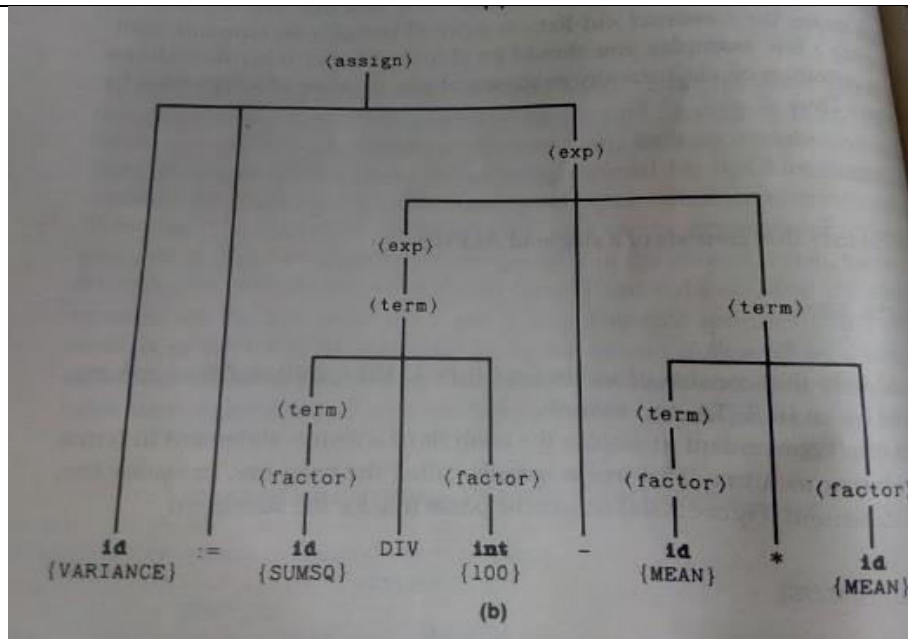
	<p>The statement M1 DATA1, DATA2 is a macro invocation statements that gives the name of the macro instruction being invoked and the arguments (M1 and M2) to be used in expanding. A macro invocation is referred as a Macro Call or Invocation.</p> <p>The program with macros is supplied to the macro processor. Each macro invocation statement will be expanded into the statement s that form the body of the macro, with the arguments from the macro invocation substituted for the parameters in the macro prototype. During the expansion, the macro definition statements are deleted since they are no longer needed. The arguments and the parameters are associated with one another according to their positions. The first argument in the macro matches with the first parameter in the macro prototype and so on.</p>			
(b)	<p><b>Describe MASM Macro Processor</b></p> <p>MASM</p> <p>The macro processor of MASM is integrated with Pass 1 of the assembler</p> <p>MASM generates the unique names of local labels in the form ??n, where n is a hexadecimal number in the range 0000 to FFFF</p> <p>.ERR: signals to MASM that an error has been detected</p> <p>EXITM: directs MASM to terminate the expansion of the macro</p> <p>&amp;: is a concatenation operator</p> <p>;; is a macro comment, serves only as documentation for the macro definition</p> <p>; is an ordinary assembler language comment, included as part of the macro expansion</p> <p>IRP: sets the macro-time variable to a sequence of values specified in &lt;...&gt;</p> <p>The statements between the TRP and the matching ENDM are generated once for each value of the variable</p>	[5]	CO3	L1
5	<p><b>Explain the different data structures used by Macro Processor</b></p> <p>The data structures required are:</p> <p>DEFTAB (Definition Table)</p> <ul style="list-style-type: none"> <li>• Stores the macro definition including macro prototype and macro body</li> <li>• Comment lines are omitted.</li> <li>• References to the macro instruction parameters are converted to a positional notation for efficiency in substituting arguments.</li> </ul>	[10]	CO3	L2

	<p>NAMTAB (Name Table)</p> <ul style="list-style-type: none"> <li>• Stores macro names</li> <li>• Serves as an index to DEFTAB</li> <li>• Pointers to the beginning and the end of the macro definition (DEFTAB)</li> </ul> <p>ARGTAB (Argument Table)</p> <ul style="list-style-type: none"> <li>• Stores the arguments according to their positions in the argument list.</li> <li>• As the macro is expanded the arguments from the Argument table are substituted for the corresponding parameters in the macro body.</li> <li>• The figure below shows the different data structures described and their relationship.</li> </ul>			
6	<p><b>Explain the Following :</b></p> <p><b>i) Generation of unique labels</b></p> <p><b>ii) Concatenation of Macro Parameters</b></p> <p><b>Generation of Unique Labels</b></p> <ul style="list-style-type: none"> <li>• It is not possible to use labels for the instructions in the macro definition, since every expansion of macro would include the label repeatedly which is not allowed by the assembler.</li> <li>• This in turn forces us to use relative addressing in the jump instructions. Instead we can use the technique of generating unique labels for every macro invocation and expansion.</li> <li>• During macro expansion each \$ will be replaced with \$XX, where xx is a two-character alphanumeric counter of the number of macro instructions expansion. For example,</li> <li>• XX = AA, AB, AC... This allows 1296 macro expansions in a single program.</li> </ul> <p><b>Concatenation of Macro Parameters</b></p> <p>There are applications of macro processors that are not related to assemblers or assembler programming. Conditional assembly depends on parameters provides MACRO &amp;COND</p> <pre> ..... IF (&amp;COND NE ,,")     part I ELSE     part II ENDIF ..... ENDM </pre> <p>Part I is expanded if condition part is true, otherwise part II is expanded. Compare operators: NE, EQ, LE, GT.</p> <p><i>Macro-Time Variables:</i></p>	[10]	CO3	L2

	<p>Macro-time variables (often called as SET Symbol) can be used to store working values during the macro expansion. Any symbol that begins with symbol &amp; and not a macro instruction parameter is considered as <i>macro-time variable</i>. All such variables are initialized to zero.</p> <p><b>If the value of this expression TRUE,</b></p> <ul style="list-style-type: none"> <li>• The macro processor continues to process lines from the DEFTAB until it encounters the ELSE or ENDIF statement.</li> <li>• If an ELSE is found, macro processor skips lines in DEFTAB until the next ENDIF.</li> <li>• Once it reaches ENDIF, it resumes expanding the macro in the usual way.</li> </ul> <p><b>If the value of the expression is FALSE,</b></p> <ul style="list-style-type: none"> <li>• The macro processor skips ahead in DEFTAB until it encounters next ELSE or ENDIF statement.</li> <li>• The macro processor then resumes normal macro expansion.</li> </ul> <p><b>WHILE-ENDW structure</b></p> <ul style="list-style-type: none"> <li>• When an WHILE statement is encountered during the expansion of a macro, the specified Boolean expression is evaluated.</li> <li>• TRUE –The macro processor continues to process lines from DEFTAB until it encounters the next ENDW statement.</li> <li>• When ENDW is encountered, the macro processor returns to the preceding WHILE, re-evaluates the Boolean expression, and takes action based on the new value.</li> <li>• FALSE – The macro processor skips ahead in DEFTAB until it finds the next ENDW statement and then resumes normal macro expansion.</li> </ul>			
7(a)	<p><b>Write short note on</b></p> <p><b>i) Linkage Editor</b></p> <p>The figure below shows the processing of an object program using Linkage editor. A linkage editor produces a linked version of the program – often called a load module or an executable image – which is written to a file or library for later execution.</p> <p>The linked program produced is generally in a form that is suitable for processing by a relocating loader. Some useful functions of Linkage editor are, an absolute object program can be created, if starting address is already known.</p> <p>New versions of the library can be included without changing the source program. Linkage editors can also be used to build packages of subroutines or other control sections that are generally used together.</p> <p>Linkage editors often allow the user to specify that external references are not to be resolved by automatic library search – linking will be done later by linking loader – linkage editor + linking loader – savings in space</p> <p><b>ii) Bootstrap Loader</b></p> <p>When a computer is first turned on or restarted, a special type of absolute loader, called bootstrap loader is executed. This bootstrap loads the first program to be run by the computer -- usually an operating system. The bootstrap itself begins at address 0. It loads the OS starting address 0x80. No header record or control information, the object code is consecutive bytes of memory.</p> <p>The algorithm for the bootstrap loader is as follows</p>	[8]	CO4	L1



	<p><b>Begin</b>  <math>X \leftarrow 0x80</math> (the address of the next memory location to be loaded)  <b>Loop</b>  <math>A \leftarrow \text{GETC}</math> (and convert it from the ASCII character code to the value of the hexadecimal digit) save the value in the high-order 4 bits of S  <math>A \leftarrow \text{GETC}</math> combine the value to form one byte <math>A \leftarrow (A+S)</math> store the value (in A) to the address in register X  <math>X \leftarrow X+1</math>  <b>End</b></p>			
(b)	<p><b>What is Recursive-Descent Parsing</b></p> <p>Recursive-Descent parsing is a top down parsing method.</p> <p>For each non-terminal, there is a procedure which</p> <p>Begins from the current token, search the following tokens, and try to recognize the rule associated with the non-terminal.</p> <p>May call other procedures or even itself for non-terminals included in the rule of this non-terminal.</p> <p>When a match is recognized, the procedure returns an indication of success, otherwise, error.</p>	[2]	CO4	L1
8(a)	<p><b>Consider the following finite automata and check whether the following strings are recognized or not</b></p> <p><b>i)abc ii) abccabc iii) ac iv)abcabc v)abcac</b></p> <pre> graph LR     Start(( )) --&gt; 1((1))     1 -- a --&gt; 2((2))     2 -- b --&gt; 3((3))     3 -- c --&gt; 4(((4)))     4 -- a --&gt; 2     4 -- c --&gt; 4   </pre> <p>i)abc - Recognized  ii) abccabc - Recognized  iii) ac - Not Recognized  iv)abcabc - Recognized  v)abcac - Not Recognized</p>	[4]	CO4	L3
(b)	<p><b>Construct Parsing Tree for following PASCAL statement</b></p> <p><b>&lt;assign&gt; ::= id := &lt;exp&gt;</b></p>	[4]	CO4	L3



(c) **What are the Basic Functions of compiler?**

[2]

CO4

L1

Basic functions of Compiler are Scanning, parsing, and (object) code generation.

1) Lexical analysis

Scan the program to be compiled and recognize the tokens (from string of characters).

2) Syntactic analysis

The source statements written by programmers are recognized as language constructs described by the grammar. This process is achieved by building the parse tree for the statements being translated.

3) Code Generation

Most compilers create machine-language programs directly instead of producing a symbolic program for later translation by an assembler.

