CMR											RELITE 25 YEARS * * *			
INSTITUTE OF			U	ISN								* CELE		
TECHNOLOGY  Internal Assesment Test – II														
Sub	Sub: Operating Systems Code: 17MCA24													
												MCA		
Date:											in:	MC	A	
	Answer Any One FULL Question from each part.													
									Mar ks	OBE	RBT			
	Part - I										KS	CO	KD1	
1 (a)	Compare preemptiv	ve with non	nreemnt								3	CO2	L1	
	Discuss about the O			· · · · · · · · · · · · · · · · · · ·								CO2	L1 L2	
` /											7			
	Explain the five state process model with transition diagram, showing how to change process model for a suspend process										10	CO2	L2	
	μ	P1			Part – 1	II							<u> </u>	
3 (a)												CO3	L1	
	for the deadlock to occur in a system									itions	6		21	
	Write short notes about resource allocation graph										4	CO3	L1	
	Explain reader's writer's problem and write the solution using semaphore									10	CO2	L3		
	Explain reader 5 W	inter 5 proof	iem ana v		ART -		SCIIIu	риоге			10	002	23	
5 (a)	Define is response	e time and t	turnarou								3	CO2	L1	
-	Consider the follo					length	of CF	PU burs	t:		7	CO2	L3	
	Processes		P2	Р3	P4	P5								
	Burst Time	6	2	8	3	4								
	Arrival Time	2	5	1	0	4								
	Draw Gantt chart for SJF (preemptive) and SFJ (non-preemptive.													
	Find the average waiting time, average turn around time, throughput for each													
	scheduling algorithm.											COA	T 1	
6 (a)	Define is waiting time, throughput?  Consider the following set of processes with given length of CPU burst:										7	CO2	L1 L3	
	Processes	P1	P2							'	COZ	LS		
	Burst Time	10	1		2	1		5						
	Priority	3	1		3	4		2						
	All processes arrived at time 0 in the given order.													
	Draw Gantt chart using SJF (non-preemptive), Priority (Non-preemptive) [Smallest													
	number implies highest priority], and Round Robin [Quantum-2 ms] scheduling													
	policies. Find the average waiting time for each scheduling policy.													
Part – IV  7 (a) What is a thread? Explain the benefits of a multithreaded programming 4 CO2													L1	
	Explain with diag								<u> </u>		6	CO2	L2	
8				ring: 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1								CO3	L1	
	Assuming 3 frames, find the number of page faults when the following algorithms are used i) LRU ii) FIFO iii) Optimal. Note that initially all the frames are empty.										10			
	μ) LRU ii) FIFO iii)	Optimal. N	ote that i	•	all the fr Part –		re em	npty.						
9	What is demand paging? Explain how TLB improves the performance of demand paging										10	CO3	L1	
	with neat diagram									10		1/1		
10.	State the dining philosophers problem and give solution for the same, using									10	CO3	L3		
	semaphores													