## 2002 SCHEME

USN											CS45
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## Fourth Semester B.E. Degree Examination, December 2012

	Introduction to Microprocessor											
T	ime:	: 3 hrs. Max. Ma	rks:100									
		Note: Answer any FIVE full questions.										
1	a. b.	Wish a timing discussion of the state of the	(10 Marks) (10 Marks)									
2	a. b.	Explain the instruction template for MOV instruction. Generate binary code for M	(10 Marks) IOV, AX, (10 Marks)									
3	a. b.	i) XLAT ii) DAA iii) CMP iv) LEA What are assembler directives? Explain the following assembler directives:	(10 Marks) 10 Marks)									
4	a. b. c.	Differentiate between macros and procedures.  Write an ALP to generate time delay of 100 msec, using 8086 microprocessor with	06 Marks) 06 Marks) 10 MHz 08 Marks)									
5	a. b.	sort method.	g Bubble 10 Marks) 10 Marks)									
6	a. b.	Familia (n. 4) 1.27 / COOMA 1.1 . 1.1 . 1.1	ording to  O Marks)  O Marks)									
7	a.	Explain the structure of interrupt vector table of 8086/8088 µp. Explain hardware i	nterrupt.									
	b.	(1	0 Marks) 0 Marks)									
8	a. b.	Find out control word for 8255 PPI for mode 0 with port A as input and ports B, C a	8 Marks) is output 4 Marks)									

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clockwise by 4 rotation

Explain a typical stepper motor interface and write an ALP to rotate 4 phase stepper motor

(08 Marks)