## 2002 SCHEME

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## Fourth Semester B.E. Degree Examination, June/July 2011 Introduction to Microprocessors

		Introduction to Microprocessors								
Time: 3 hrs.  Note: Answer any FIVE full questions.  Max. Marks:10										
1	b.	With a neat figure, explain the architecture of 8085 microprocessor. Explain the ways of writing into cache. Give any three differences between I/O mapped I/O and the memory mapped I/O.	(10 Marks) (06 Marks) (04 Marks)							
2	a. b.	Briefly explain the following with respect to 8086 processor:  i) Execution unit  ii) Bus interface unit.  Explain the function of the following signals in 8086:  i) DEN  ii) INTA  iii) TEST  iv) RD  v) LOCK	(10 Marks) (10 Marks)							
3		Explain the following address modes by giving their physical address formation: i) Direct address ii) Based indexed iii) Relative based indexed. What are the single bit indicators used in opcode field? Explain. Give set up instructions to multiply a number (i) by 10 (ii) by 20, without u instructions.	(06 Marks) (08 Marks) Ising MUL (06 Marks)							
4		Explain the following with suitable examples: i) DW ii) ENDP iii) SHORT Write a program to compute the average of any number of bytes in an array in menumber of bytes to be added in the 1 <sup>st</sup> byte of the array. Explain the AAM instruction, with an example.	(09 Marks) emory. The (08 Marks) (03 Marks)							
5	a. b. c.	Write a program to count the number of vowels in a given string.  With an example, explain how the MACROS can be defined and called be parameters to MACROS.  Write a program to generate a delay of 100 ms using 8086 system that runs at 5 M	(05 Marks)							
6	а. b.	Design an 8086 based system with the following specifications: i) 8086 in minimum node ii) 1M byte of RAM iii) Starting address of RAM is Explain the interfacing of stepper motor to 8086 processor?	00000H (10 Marks) (10 Marks)							
7	a. b.	With a figure, explain the internal architecture of 8255.  Explain the control word format of 8255 in I/O mode. Also, give the control word the following configuration:  i) PA - O/P port ii) PB - I/P port iii) Mode 0	(10 Marks)							
8	a. b. e.	Write short notes on: Control of high power devices with 8255 Minimum mode operation of 8036 Instruction format.	(06 Marks) (07 Marks) (07 Marks)							

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