CMR INSTITUTE OF	18EE53 - POWER ELECTRONICS		1	14
TECHNOLOGY	Scheme & Solution			CMR
	Internal Assesment Test - II			
	Answer Any FIVE FULL Questions			
		Marks	CO	E RBT
1 Explain single phase ha	If wave controlled converter with RL load and	[10]	CO4	L2
freewheeling diode, deriv	e the expression for average and RMS voltage.			
Vs = Vm sin ωt	FD VO	2		
Vo +Vm	2π 2π ω t 2π ω t ω t ω t	2		
During the positive half cycle				
	of a thyristor T, a voltage source Vs, a diode FD			
	an inductive load L and a resistive load R.			
 During the positive 	half cycle of the input voltage, the thyristor T is			
forward biased but i	t does not conduct until a gate signal is applied to			
it.				
When a gate pulse:	is given to the thyristor T at t= , it gets turned			
	-			

ON and begins to conduct.

 When the thyristor is ON, the input voltage is applied to the load but due to the inductor present in the load, the current through the load builds up slowly.

During the negative half cycle

- During the negative half cycle, the thyristor T gets reverse biased.
- At this instant i.e at t = , the load current shift its path from the thyristor to the freewheeling diode.
- When the current is shifted from thyristor to freewheeling diode, the thyristor turns OFF.
- The current through the inductor slowly decays to zero through the loop freewheeling diode-R-L.
- So here the thyristor will not conduct in the negative half cycle and turns off at t= .
- So the load receives voltage only during the positive half cycle.
- The average value of output voltage can be varied by varying the firing angle .

Average Output Voltage

Average Value of Load output Voltage

$$= (1/2\pi) \int_{\alpha}^{\pi} VmSin\omega t d(\omega t)$$

$$= (Vm/2\pi) \int_{\alpha}^{\pi} Sin\omega t d(\omega t) = \frac{V_m}{2\pi} \left[-Cos(\omega t) \right]_{\alpha}^{\pi}$$

$$= \left(\frac{Vm}{2\pi} \right) [1 + Cos\alpha]$$

For Single Phase Half Wave Controlled Rectifier:

Average Value of Load output Voltage

$$VdC = \left(\frac{Vm}{2\pi}\right)[1 + Cos\alpha]$$

	RMS Value of Output voltage	2		
	RMS Value of Load output Voltage			
	$= \sqrt{(1/2\pi) \int_0^{2\pi} [VmSin\omega t]^2 d(\omega t)}$			
	$=\sqrt{(Vm/4\pi)\int_{\alpha}^{\pi}[1-Cos2\omega t]d(\omega t)}$			
	$=\left(\frac{Vm}{2\sqrt{\pi}}\right)\sqrt{(\pi-\alpha)+(1/2)Sin2\alpha} = \left(\frac{Vm}{2}\right)\sqrt{\frac{1}{\pi}\left\{(\pi-\alpha)+(1/2)Sin2\alpha\right\}}$			
	RMS Value of Load output Voltage			
	$Vrms = \left(\frac{Vm}{2\sqrt{\pi}}\right)\sqrt{(\pi-\alpha) + (1/2)Sin2\alpha}$			
2	Explain single phase fully controlled converter with RLE load with neat	[10]	CO4	L2
	diagram and waveform and derive the expression for average and RMS			
	voltage.			
	V_{dc} V	2		
	V_{S} V_{S	2		

During the positive half-cycle

- The fully controlled bridge converter consists of four thyristors *T1*, *T2*, *T3* and *T4* connected in the form of full wave bridge configuration.
- Each thyristor is controlled and turned on by its gating signal and naturally turns off when a reverse voltage appears across it (Line commutation or Natural commutation).
- Thyristors T1 and T2 are forward biased (0 to);
- when these two thyristors are turned on simultaneously at t = 0, the load is connected to the input supply through T1 and T2.
- Due to the inductive load, thyristors T1 and T2 continue to conduct beyond t =, even though the input voltage is already negative.
- T1 and T2 conduct from t = to +
- The output voltage across the load follows the input voltage vo = vm
 sin t

During the negative half-cycle of the input voltage

- Thyristors T3 and T4 are forward biased (to 2);
- Turning on of thyristors *T*3 and *T*4 applies the supply voltage across thyristors *T*1 and *T*2 as reverse blocking voltage.
- T3 & T4 are triggered at t = +
- T1 and T2 are turned off due to line or natural commutation.
- The output voltage across the load follows the input voltage
 vo = vm sin t
- Load current is transferred from T1 and T2 to T3 and T4.
- T3 and T4 conduct from t = + to 2 +
- During next half cycle T3 and T4 are turned off

During Period from to - Rectification mode

- The input voltage vs and input current is are positive,
- Power flows from the supply to the load.
- The converter is said to be operated in *rectification* mode.

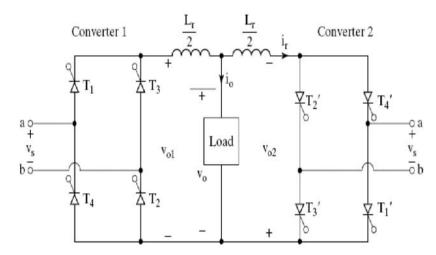
During Period from to + - *Inversion* mode

- The input voltage vs is negative and the input current is is positive,
- Reverse power flows from the load to the supply.

	The converter is said to be operated in inversion mode.			
	• This converter is extensively used in industrial applications up to 15			
	kW.			
	 Depending on the value of , the average output voltage could be 			
	either positive or negative and it provides two-quadrant operation.			
	Average Output Voltage Vdc			
		2		
	$V_{O(dc)} = V_{dc} = \frac{2}{2\pi} \left[\int_{\alpha}^{\pi+\alpha} V_m \sin \omega t. d(\omega t) \right]$	2		
	$V_{O(dc)} = V_{dc} = \frac{1}{\pi} \left[\int_{\alpha}^{\pi+\alpha} V_m \sin \omega t. d(\omega t) \right] $ $V_{O(dc)} = V_{dc} = \frac{V_n}{\pi} \left[-\cos(\pi + \alpha) + \cos\alpha \right]$			
	$V_{O(dc)} = V_{dc} = \frac{1}{\pi} \left[\int_{\alpha}^{\pi} V_m \sin \omega t. \alpha(\omega t) \right]$ $\cos(\pi + \alpha) = -\cos \alpha$			
	$V_{O(dc)} = V_{dc} = \frac{V_m}{\pi} \left[\int_{\alpha}^{\pi+\alpha} \sin \omega t. d(\omega t) \right]$ Therefore $V_{O(dc)} = V_{dc} = \frac{2V_m}{\pi} \cos \alpha$			
	$V_{O(dc)} = V_{dc} = \frac{V_m}{\pi} \left[-\cos \omega t \right]_{\alpha}^{\pi + \alpha}$			
	RMS Output Voltage Vrms	2		
	The rms value of the output voltage is given by			
	$V_{\rm rms} = \left[\frac{2}{2\pi} \int_{\alpha}^{\pi + \alpha} V_m^2 \sin^2 \omega t d(\omega t)\right]^{1/2}$			
	$= \left[\frac{V_m^2}{2\pi} \int_{\alpha}^{\pi+\alpha} (1-\cos 2\omega t) \ d(\omega t)\right]^{1/2}$			
	$=\frac{V_m}{\sqrt{2}}=V_s$			
	$\sqrt{2}$			
	With a purely resistive load, thyristors T_1 and T_2 can conduct from α to π , and thyristors			
	T_3 and T_4 can conduct from $\alpha + \pi$ to 2π .			
3	Illustrate the single phase dual converter with neat diagram and waveform	[10]	CO4	L2
	and also explain the significance of circulating current in dual converter.			
	• Dual converter- the name itself indicates that it has two converters in			
	it.			
	The dual converter system will provide four quadrant operation			
		1	1	

- Normally used in high power industrial variable speed drives.
- In the case of a single phase full converter with inductive loads, the converter can operate in two different quadrants in the *Vdc* versus *Idc* operating diagram.
- If two single phase full converters are connected in parallel and in opposite direction (connected in back to back) across a common load four quadrant operation is possible.

Such a converter is called as a dual converter.



 Two single phase full converters are connected in parallel and in opposite direction (connected in back to back) across a common load

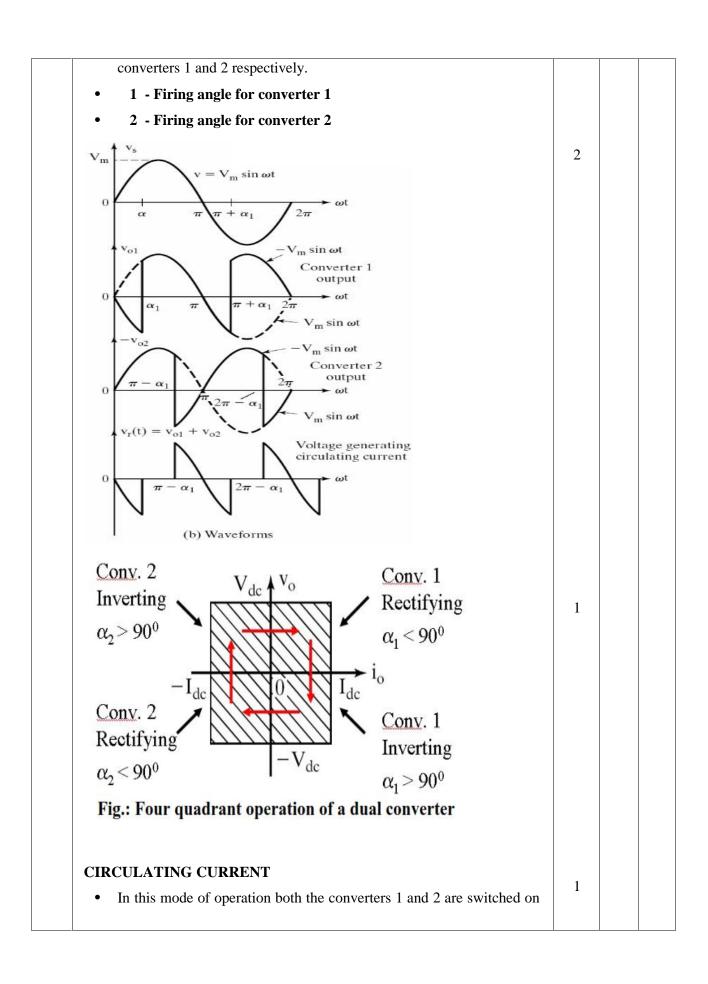
Converter 1

- The converter number 1 provides a positive dc output voltage and a positive dc load current, when operated in the rectification mode.
- The converter number 1 provides a negative dc output voltage and a positive dc load current, when operated in the inverter mode.

Converter 2

- The converter number 2 provides a negative dc output voltage and a negative dc load current when operated in the rectification mode.
- The converter number 2 provides a positive dc output voltage and a negative dc load current when operated in the inverter mode.
- We can have bi- directional load current and bi-directional dc output voltage.
- The magnitude of output dc load voltage and the dc load current can be controlled by varying the trigger angles 1 & 2 of the

2

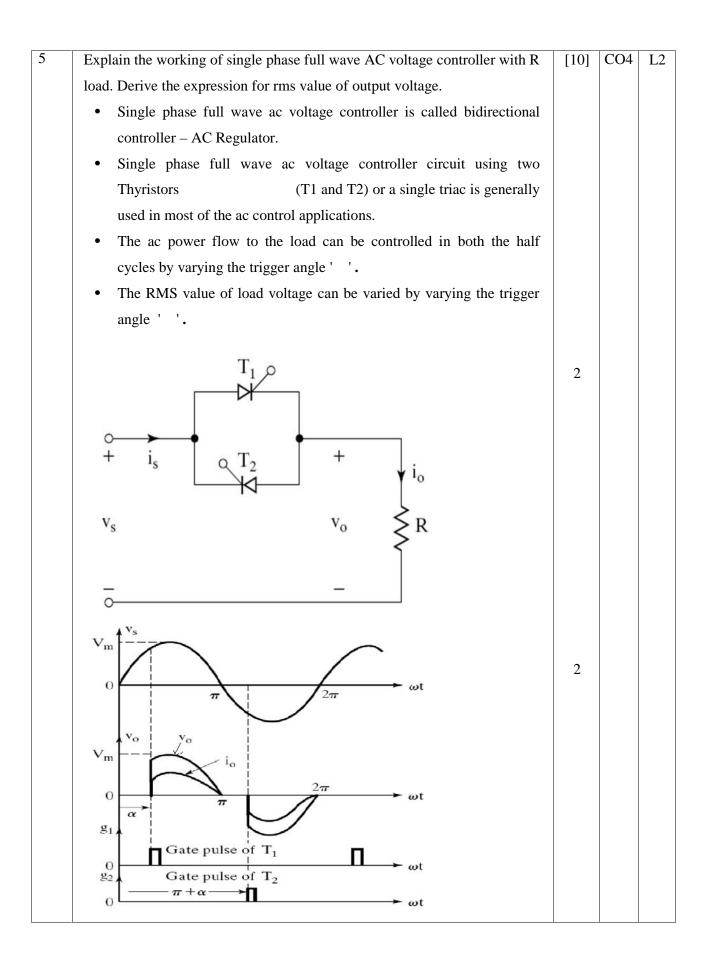


	and operated simultaneously and both the converters are in a			
	state of conduction.			
	If converter 1 is operated as a controlled rectifier by adjusting the			
	trigger angle $_1$ between 0 to 90° (0< $_1$ < 90°)			
	The second converter 2 is operated as a line commutated inverter by			
	increasing its trigger angle 2 above 90 (90°< 2<180°).			
	• The trigger angles 1 and 2 are adjusted such that they produce the			
	same average dc output voltage across the load terminals.			
	• Instantaneous output voltages of two converters are out of phase, this			
	voltage difference causes circulating current between two converters.			
	This cannot flow through the load, and it is limited by limiting			
	reactor.			
	In the circulating current mode a current builds up between the two			
	converters even when the load current falls to zero.			
	In order to limit the circulating current flowing between the two			
	converters, we have to include current limiting reactors in series			
	between the output terminals of the two converters.			
	Significance of circulating current	2		
	We can have faster reversal of load current as the two converters are			
	in a state of conduction simultaneously.			
	This greatly improves the dynamic response of the output giving a			
	faster dynamic response.			
	The output voltage and the load current can be linearly varied by			
	adjusting the trigger angles 1 and 2 to obtain a smooth and linear			
	output control.			
	The control circuit becomes relatively simple.			
	Output response is very fast.			
	The load current is free to flow in either direction at any time.			
	The reversal of the load current can be done in a faster and smoother way.			
4	A single phase circulating current Dual converter is fed by a single phase	[10]	CO4	L3
	120V, 50Hz supply. The load is resistive. The peak current of converter 1			
	is 35 A. The firing angles are 30 and 150 respectively. If peak circulating			

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current is 13A, Find (i) Inductance of current limiting reactor (ii) Load
Resistance.
 4) Circulating Curent mode dual converter:
        Ve = 120 V, Vm = V2 Vs = V2 x 120 = 169.9 V
        f = 50Hz ; R load.
        W=2xf=2x3.14 x50= 314.16 nad/sec.
       Peak Curent of converse 1 = 35A.
       X1 = 30°; X2 = 150°.
      Peak Circulating Coment Irman = 13A
 (i) Industance of Curent limiting Reador Lr.
                                                             5
    Inman = 2 Vm (1-cosx1) = 2x 169.7 (1-cos 30)
                                  314-16xLx
         13 = 339.4 x 0.134
                                                (5 marks
                314.16xLx
      L_{x} = \frac{339.4 \times 0.134}{314.16 \times 13} = 0.01113H
      Lr= 11.13 mH
                                           (5 mancs)
(jj) Load Resistance R.
                                                             5
   Peak current of conveners = Irman + Ip
      5% 35 = 13 + IP
          Tp = 35-13 = 22 A.

\frac{Tp = \frac{Vm}{R}}{R} \Rightarrow R = \frac{Vm}{Tp} = \frac{169.7}{22} = 7.71 \text{ N}.

R = 7.71 W
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During Positive Half Cycle t = 0 to

- The thyristor T1 is forward biased during the positive half cycle of input ac supply.
- It can be triggered and made to conduct by applying a suitable gate trigger pulse ' '. only during the positive half cycle of input supply.
- When T1 is triggered it conducts and the load current flows through the thyristor T1, the load and through the transformer secondary winding.
- By assuming T1 as an ideal thyristor switch it can be considered as a closed switch when it is ON during the period t = to radians.
 Output Voltage Vo = Vs
- The output voltage across the load follows the input supply voltage when the thyristor T1 is turned-on and when it conducts from t = to radians.
- When the input supply voltage decreases to zero at t= , for a resistive load the load current also falls to zero at t=
- Thyristor T1 turns off naturally at t = 0. Hence load current also zero at t = 0

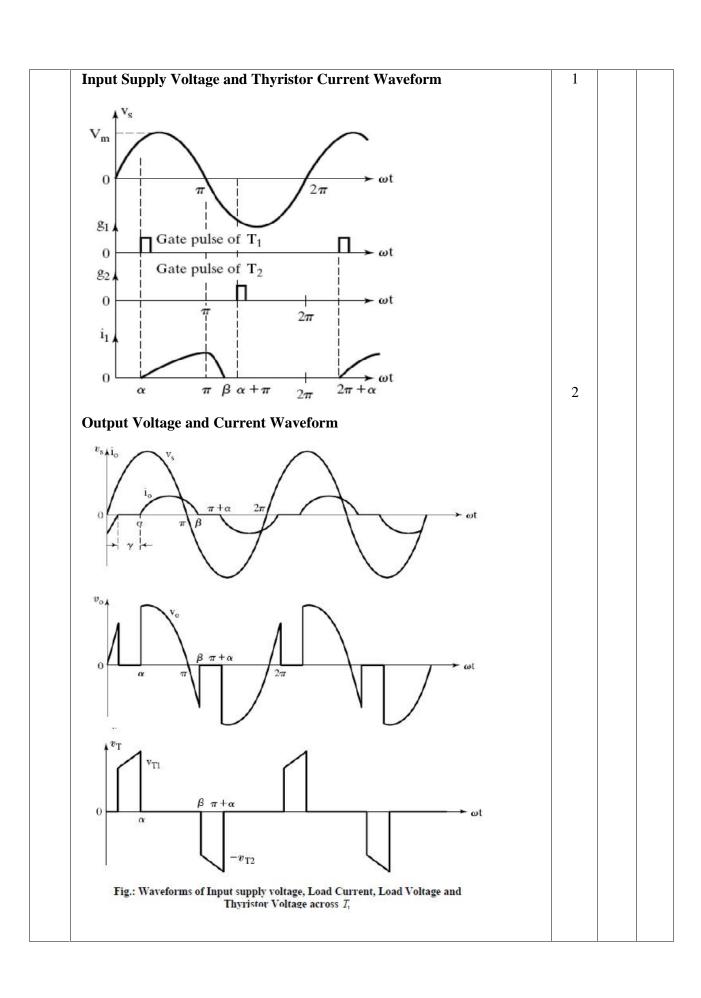
During Negative Half Cycle t = to 2

- Between the time period to t = to 2,
- The thyristor T2 is forward biased during the negative cycle of input supply
- Thyristor T2 is triggered at a delay angle (+)
- The output voltage follows the negative halfcycle of input from t = + to 2.
- When T2 is ON, the load current flows in the reverse direction (upward direction) through T2 during t = + to 2.
- The time interval (spacing) between the gate trigger pulses of T1 and T2 is kept at radians or 180 degrees.
- Thyristor T2 turns off naturally at t = 2. Hence load current also zero at t =

Why it is bidirectional Controller?

➤ Power flow is controlled during the positive half cycle and negative

half cycle of input voltage by firing pulses to the thyristors T1 and			
T2, hence it is called as bidirectional controller.			
EXPRESSION FOR RMS OUTPUT VOLTAGE VO(RMS)	3		
$v_S = V_m \sin \omega t = \sqrt{2}V_S \sin \omega t$			
$V_o = \sqrt{\frac{2}{2\pi} \int_{\alpha}^{\pi} 2V_s^2 \sin^2 \omega t d(\omega t)}$			
$=\sqrt{\frac{4V_s^2}{4\pi}}\int_{\alpha}^{\pi}(1-\cos 2\omega t)d(\omega t)$			
$=V_{s}\sqrt{\frac{1}{\pi}}\left(\omega t\right)/\left(\frac{\sin 2\omega t}{2}\right)/\left(\frac{\pi}{\alpha}\right)$			
$= V_s \left[\frac{1}{\pi} \left[(\pi - \alpha) - \left\{ \frac{\sin 2\pi}{2} - \frac{\sin 2\alpha}{2} \right\} \right] \right]$ $; \sin 2\pi = 0$			
$V_o = V_s \sqrt{\frac{1}{\pi} \left(\pi - \alpha + \frac{\sin 2\alpha}{2}\right)}$			
6 Explain the working of single phase full wave AC voltage controller with	[10]	CO4	L2
RL load. Derive the expression for rms value of output voltage.			
T_1	1		
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			
$\left\{\begin{array}{c} v_{o} \\ \end{array}\right\}_{L}$			
_ 3			
0			
Fig: Single phase full wave ac voltage controller with RL load			



During Positive Half Cycle t = 0 to

- The thyristor T1 is forward biased during the positive half cycle of input ac supply.
- It can be triggered and made to conduct by applying a suitable gate trigger pulse ' '. only during the positive half cycle of input supply.
- When T1 is triggered it conducts and the load current flows through the thyristor T1, the load and through the transformer secondary winding.
- By assuming T1 as an ideal thyristor switch it can be considered as a closed switch when it is ON during the period t = to radians.
 Output Voltage Vo = Vs
- Due to the inductance in the load, the load current i0 flowing through
 T1 would not fall to zero at t = , when the input supply voltage starts to become negative.
- The thyristor *T*1 will continue to conduct the load current until all the inductive energy stored in the load inductor L is completely utilized and the load current through *T*1 falls to zero at t = .
- is referred to as the Extinction angle, (the value of t) at which the load current falls to zero.

Conduction Period of T1 t = to

- is referred to as the Extinction angle, (the value of t) at which the load current falls to zero.
- The thyristor T1 conducts from t = to.
- The conduction angle of T1 is =(), which depends on the delay angle and the load impedance angle .
- Thyristor T1 turns off naturally at t = ...
- Hence load current flows from at t = to.
- is the extinction angle which depends upon the load inductance value.

During Negative Half Cycle t = to 2

- Between the time period to t = to 2,
- The thyristor *T*2 is forward biased during the negative cycle of input supply

- Thyristor T2 is triggered at a delay angle (+)
- The output voltage follows the negative half cycle of input from t = + to 2.
- When T2 is ON, the load current flows in the reverse direction (upward direction) through T2 during t = + to 2 + , because of inductive load
- The time interval (spacing) between the gate trigger pulses of T1 and T2 is kept at radians or 180 degrees.
- Thyristor T2 turns off naturally at t = 2 + ...
- Hence load current flows from $\ t=+\ to\ 2+$, due to conduction of T2

EXPRESSION FOR RMS OUTPUT VOLTAGE VO(RMS)

$$V_{O(RMS)} = \left[\frac{1}{\pi} \int_{\alpha}^{\beta} V_m^2 \sin^2 \omega t. d(\omega t)\right]^{\frac{1}{2}}$$

Output $v_o = V_m \sin \omega t$, for $\omega t = \alpha$ to β , when T_1 is ON.

$$V_{O(RMS)} = \left[\frac{{V_m}^2}{\pi} \int_{\alpha}^{\beta} \frac{\left(1 - \cos 2\omega t\right)}{2} d\left(\omega t\right)\right]^{\frac{1}{2}}$$

$$V_{O(RMS)} = \left[\frac{V_m^2}{2\pi} \left\{ \int_{\alpha}^{\beta} d(\omega t) - \int_{\alpha}^{\beta} \cos 2\omega t. d(\omega t) \right\} \right]^{\frac{1}{2}}$$

$$V_{O(RMS)} = \left[\frac{V_m^2}{2\pi} \left\{ (\omega t) \middle/ \frac{\beta}{\alpha} - \left(\frac{\sin 2\omega t}{2}\right) \middle/ \frac{\beta}{\alpha} \right\} \right]^{\frac{1}{2}}$$

$$V_{O(RMS)} = \left[\frac{V_m^2}{2\pi} \left\{ (\beta - \alpha) - \frac{\sin 2\beta}{2} + \frac{\sin 2\alpha}{2} \right\} \right]^{\frac{1}{2}}$$

$$V_{O(RMS)} = V_m \left[\frac{1}{2\pi} \left\{ (\beta - \alpha) + \frac{\sin 2\alpha}{2} - \frac{\sin 2\beta}{2} \right\} \right]^{\frac{1}{2}}$$

$$V_{O(RMS)} = \frac{V_m}{\sqrt{2}} \left[\frac{1}{\pi} \left\{ (\beta - \alpha) + \frac{\sin 2\alpha}{2} - \frac{\sin 2\beta}{2} \right\} \right]^{\frac{1}{2}}$$

A single phase full wave AC voltage controller has a resistive load of	[10]	CO4	L3
R=20 and the input voltage is Vs= 230 V (rms), 50 Hz. The delay angle			
of thyristor T1 and T2 are equal $_1 = _2 = /2$. Determine (i) RMS output			
voltage V_o (ii) The input Power Factor P.F. (iii) The average thyristor current I_A (iv) The rms value of thyristor current.			
M) Single Phase Full wave AC Notherse Controller.			
Vs= 2301 (Rms); f=50Hz; R=201			
$\alpha_1 = \alpha_2 = \pi_2$.			
(i) Rms Cutput Voltage. Vo.	3		
$V_0 = V_S \left[\frac{1}{\pi} \left(\pi - \alpha \right) + \frac{8\pi i}{2} \frac{2}{\alpha} \right]^2$			
= VS (T - R2) + 8m 2x R/2 20.			
$= V_{S} \left[\frac{1}{\pi} \left(\pi - \frac{\pi}{2} \right) + \frac{9 \sin 2 \times \frac{\pi}{2}}{2} \right]^{\frac{1}{2}}$ $= 230 \left[\frac{1}{\pi} \times \frac{\pi}{2} \right]^{\frac{1}{2}}$ $= 230 \left[\frac{1}{\pi} \times \frac{\pi}{2} \right]^{\frac{1}{2}}$ $= 162.23 \text{ V}$ $V_{L} = 162.23 \text{ V}$			
0- (02.23V.)			
ii. Input Power Factor = $Vo / Vs = 162.23 / 230 = 0.705 lag$	2		
(iii) Average Thyristor Curent ID.	2		
Itang = $\frac{V_m}{2\pi R}$ (1+cosa) $V_m = \sqrt{2} V_s$	_		
$= \sqrt{2} \times 230 \left[1 + \cos 90 \right]$			
Travg => 2.59 A.			

(iv) Rms Thy risher Current.

To Rms = $\frac{V_S}{\sqrt{2}R} \left(\frac{1}{\pi} (\pi - x) + \frac{3}{2} \frac{in_2 x}{2} \right)^2$ $= \frac{230}{\sqrt{2} \times 20} \times \left[\frac{1}{\pi} (\pi - \frac{\pi}{2}) + \frac{3}{2} in_2 \times \frac{\pi}{2} \right]^2$ $= \frac{230}{\sqrt{2} \times 20} \times \left[\frac{1}{\pi} \times \frac{\pi}{2} \right]^2 = \frac{230}{\sqrt{2} \times 20} \times \frac{1}{\sqrt{2}}$ $= \frac{230}{\sqrt{2} \times 20} \times \left[\frac{1}{\pi} \times \frac{\pi}{2} \right]^2 = \frac{230}{\sqrt{2} \times 20} \times \frac{1}{\sqrt{2}}$ $= \frac{1}{\sqrt{2} \times 20} \times \left[\frac{1}{\pi} \times \frac{\pi}{2} \right]^2 = \frac{230}{\sqrt{2} \times 20} \times \frac{1}{\sqrt{2}}$