USN

Sixth Semester B.E. Degree Examination, June/July 2017 **Embedded Systems**

Max. Marks: 100 Time: 3 hrs.

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

PART - A			
1	a. b. c.	Define Embedded system. Explain the main components of embedded system. (07 Markstexplain the skill required for small scale, medium scale and sophisticated embedded system design. (08 Markstexplain the architecture of 6808 and 6811. (05 Markstexplain the skill required for small scale, medium scale and sophisticated embedded system design.	m s)
2	a. b.	Explain the applications of embedded systems. Draw the block diagram of Embedded systems SOC for celless bar code scanner and explaint's features and benefits. (10 Marks)	in
3	a.	Explain the interfacing of DAC to micro-processor system. (07 Marks)	s)
	b.	Explain the following: i) Basic Data Acquisition system ii) Signal Conditioning using DSP. (08 Marks) (05 Marks)	
4	a.	Explain the issues observed in embedded system design. (10 Marks)	
	b.	Briefly describe the top-down design process. (05 Marks	~
	c.	Write a note on Thermal considerations. (05 Marks)	s)
PART - B			
5	a.	With a neat diagram, explain the components of an operating system. (05 Marks	s)
	b.	Explain services provided by the Kernel. (10 Mark	
	c.	Mention the advantages of 'C' as a High level language. (05 Marks)	s)
6	a. b.	Briefly describe function queue scheduling architecture. Mention advantages and disadvantages of Round-Robin with Interrupt architecture. (10 Mark Explain the classification of Real-time operating system (RTOS) and describe its feature	s) S.
		(10 Mark	3)

- Define with respect to serial I/O.
 - Full duplex communication
 - Half duplex communication
 - Simplex communication
 - Baud rate iv)

(10 Marks) Frame. v)

- Explain three basic approaches of interfacing multiple keys to a single 8-bit parallel port. (10 Marks)
- Explain the software Debouncing. With a help of flowcharts and 'C' code. (10 Marks) 8 a.
 - Explain architecture of a computer with memory mapped I/O and isolated I/O with aid of (10 Marks) neat diagram.

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