

18MCA34

Third Semester MCA Degree Examination, July/August 2022 **System Software**

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

- Write a sequence of instructions for SIC/XE and set the elements of an array ALPHA of size 1 100 words to 0. Use immediate addressing, registers - to - register instruction to make process efficient. (04 Marks)
 - b. Explain the instruction formats and addressing modes clearly indicating the settings of flag bits with reference to SC/XE machine. (08 Marks)
 - What are the basic functions of assembler? Explain the various data structures used by it with example. (08 Marks)

- Write a sequence of instructions for SIC to set 20-byte string to blank. (04 Marks)
 - Describe the memory, registers, data formats and instruction format of VAX machine.

(08 Marks)

- c. Determine the target address for the following instruction (hex)
 - i) 032600
- ii) 03C300 iii) 022030
- v) 003600 given (B) = 006000 (PC) = 03000 (X) = 000090.

(08 Marks)

Module-2

- Write the formats of header and text records with example for each. 3
 - Translate the following assembly language program to object program, clearly showing the symbol table entries.

COPY START LDT #3 🎤 #0 LDX MOVECH STR1, X LDCH STCH STR2, X TIXR T JLT **MOVECH BYTE** C 'EOF' STR1 RESB 3 **END FIRST**

LDT - 74, LDX - 04, LDCH - 50, STCH - 54, TIXR - B8, JLT - 38, T - 5, X - 1.

(08 Marks)

Write a note on following: i) Literals ii) MASM assembler.

(08 Marks)

OR

Compare literal and immediate operand.

(04 Marks)

Explain with example, the object code generation of statements with different instruction (08 Marks) formats and addressing modes.

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Demonstrate the working of assembler in handling the program blocks.

(08 Marks)



Module-3

Write algorithm of the absolute loader. (04 Marks) Discuss the two methods of specifying the relocation as a part of object program and also tell why modification record scheme is not convenient way for all machine architectures. (08 Marks) (08 Marks) Briefly explain the features of Loader. OR Write algorithm of SIC/XE relocating loader. (04 Marks) Demonstrate the working of linking loader with the neat diagram. (08 Marks) b. (08 Marks) Describe the features of MS – DOS Linker. Module-4 What is Macro Processor? Give its function with example. (04 Marks) Consider the following macro definition RDBUFF. & INDEV, &BUFADR, &RECLTH, &EOR, &MAX **RDBUFF** MACRO IF (&EOR NE '') &EORCK SET **ENDIF CLEAR** CLEAR (&EORCK EQ = X '&EORLDCH **RMO** A, S **ENDIF** IF (MAX EQ' +LDT #4098 **ELSE** +LDT #&MAX **ENDIF** \$LOOP TD =X' '&INDEV **JEQ** \$LOOP RD =X '&INDEV **MEND** Expand the macro for the following macro invocations RDBUFF F3, BUF, RECL, 04, 2048 RDBUFF 0E, BUFFER, LENGTH, ,80 (08 Marks) Write short notes on: i) Keyword macro parameters BANGALORE - 560 037 (08 Marks) ii) General purpose macro processors. OR Briefly explain concatenation of macro parameters with example. (04 Marks) Explain the datastructures used by the macroprocessor with example. (08 Marks) Describe the features of MASM Macro processor with appropriate example. (08 Marks)

Module-5

- 9 a. List and brief the steps involved in compilation process with example. (08 Marks)
 - b. Explain recursive decent parsing technique with the example of READ statement. Write the procedures and graphical representation of process. (08 Marks)
 - c. Write a note on:
 - i) P code compliers
 - ii) Complier compliers.

(04 Marks)

OR

- 10 a. Using the given finite automation, check if the following string are recognized or not:
 - i) A123 ii) NIEH
- iii) PIE_CHART
- iv) C__

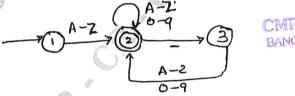


Fig.Q10(a)

(08 Marks)

- b. What is code optimization? Explain the various techniques used by the complier for code optimization. (08 Marks)
- c. Write short notes on:
 - i) Interpreters
 - ii) Compilation of structured variables.

(04 Marks)