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Internal Assessment Test 1 – May 2022

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Sub:	Software Tes	sting				Sub Code:	18IS62	Bran	nch:	ISE		
Date:	06/05/2022	Duration:	90 min's	Max Marks:	50	Sem/Sec:	VI A, B & C	1			OE	BE.
		Answ	er any FIV	VE FULL Qu	<u>estio</u>	<u>ns</u>			MA	RKS	CO	RBT
1a)	Differentiate E	Error, Fault, a	and Failure v	with example.					4	4	CO1	L2
1b)	What is the use	e of Venn di	agram in tes	ting? Explain w	vith d	iagram.			(6	CO1	L2
2a)	Compare spec	ification test	ting with co	de based testing	3				4	4	CO2	L2
2b)	Write and expusing Normal			on of Triangle s.	prob	lem with ge	nerated test c	ases	(6	CO1	L2
3a)	Explain Test a	nd Debug C	ycle with a r	neat diagram.					(6	CO1	L2
3b)	Differentiate w	hite box tes	ting and blac	ck box testing w	vith e	xample.			4	4	CO1	L2
4	Write the test cases for the C function which takes two integers as input and finds the maximum of the two integers using Robust Boundary Value analysis, and Worst case Boundary Value analysis. Assume the inputs are in the range of 1 to 35000.								1	0	CO1	L2
5a)	Explain Test generation strategies with diagram									5	CO1	L2
5b)	Explain how to write a Oracle program for GUI with example. Draw the state diagram for the same.									5	CO1	L2
6	Explain in danalysis with			oundary value	analy	vsis, Robust	Boundary v	alue	1	.0	CO1	L2

Faculty Signature CCI Signature HOD Signature

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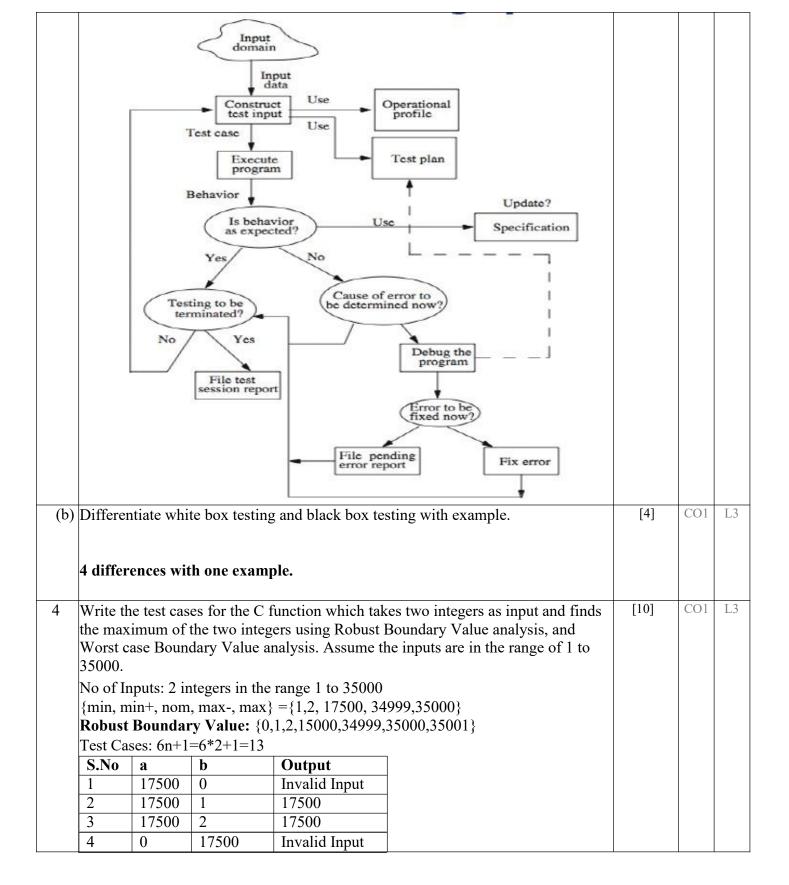
Internal Assessment Test 1 – May 2022

Date 06/05/2022 Duration 90 min's Max Marks 50 Sem/Sec VI A,B&C OBE			IIIteIIIai	ASSESSIFICITE TES	1 1viay 20					
Answer any FIVE FULL Questions	Sub:	Software Testing-Scheme and	Solutions	S	Sub Code:	18IS62	Branch:	ISE		
Differentiate Error, Fault, and Failure with example Definition:2 marks Example: 2 marks	Date:	06/05/2022 Duration: 9	0 min's	Max Marks: 50	Sem/Sec:	VI A,B&C			OE	BE
Definition:2 marks Example: 2 marks Example: 2 marks Error Fault Failure When fault code is executed failure will occur. It is more precise to say that a fault is the representation of an error, where representation is the mode of expression, such as narrative text, dataflow diagrams, hierarchy charts, source code, and so on. Error in business logic in Requirements. discount Fault in SRS and in Requirements. discount Fault in SRS and in discount calculation For customer		Answ	er any FI	VE FULL Question	<u>s</u>		M	ARKS	СО	RBT
Human Mistake, or bugs A fault is the result of an error. It is more precise to say that a fault is the representation of an error, where representation is the mode of expression, such as narrative text, dataflow diagrams, hierarchy charts, source code, and so on. Error in business logic in Requirements, discount 15% instead of 10% for purchase Rs. 10000 By What is the use of Venn diagram in testing? Explain with diagram. Point 1: Imark Two diagramsand Explanation: 2.5+2.5 = 5 marks • Venn Diagrams are helpful in identifying the test cases. Venn Diagrams helps to find certain specified behaviors have not been programmed and certain programmed (implemented) behaviors have not been specified. These correspond to faults of commission and to errors that occurred after the specification was complete. • Specified and implemented program behaviors Program behaviors Program behaviors Program behaviors	1a)	Definition:2 marks Example: 2 marks								
an error. It is more precise to say that a fault is the representation of an error, where representation is the mode of expression, such as narrative text, dataflow diagrams, hierarchy charts, source code, and so on. Error in business logic in Requirements. discount 15% instead of 10% for purchase Rs. 10000 Ib) What is the use of Venn diagram in testing? Explain with diagram. Foint 1: Imark Two diagramsand Explanation: 2.5+2.5 = 5 marks Venn Diagrams are helpful in identifying the test cases. Venn Diagrams helps to find certain specified behaviors have not been programmed and certain programmed (implemented) behaviors have not been specified. These correspond to faults of commission and to errors that occurred after the specification was complete. Specified and implemented program behaviors Program behaviors Program behaviors Program behaviors Program behaviors										
What is the use of Venn diagram in testing? Explain with diagram. Point 1: 1mark Two diagrams and Explanation: 2.5+2.5 = 5 marks		Error in business logic in Requirements. discount 15% instead of 10% for	an erro precise fault is represe error, v represe mode of such as dataflo hierarc code, a	r. It is more to say that a the entation of an where entation is the of expression, a narrative text, w diagrams, hy charts, source and so on.	executed occur. Wrong re	failure will	e			
(expected) (implemented)	1b)	Point 1: 1mark Two diagrams and Explan • Venn Diagrams are helpfucertain specified behaviors (implemented) behaviors commission and to errors • Specified and implemented Program behaviors Specification	ation: 2. al in ident is have not be that occur program program	5+2.5 = 5 marks ifying the test case t been programmed been specified. The rred after the specified behaviors	s. Venn Diag l and certain p se correspond	rams helps to to programmed d to faults of	find	[6]	COI	L2

	Specified, implemented, and tested beh	aviors			
	Program behaviors				
	Specification (expected) 5 2 6 (implement of the implement of the impleme				
2a)	Compare specification testing with code based t	testing	[4]	CO2	L2
	Advantages (1) they are independent of how the software is implemented, so if the	it is sometimes called white box (or even clear box) testing. The essential difference is that the implementation(of the black box) is known and used to identify test cases. code-based testing uses the program source code (implementation) as the basis of test case identification. The ability to "see inside" the black box allows the tester to			
	implementation changes, the test cases are still useful; and (2) test case development can occur in parallel with the implementation, thereby reducing the overall project development interval. Disadvantages: specification based test cases frequently suffer from two problems: significant redundancies may exist among	identify test cases on the basis of how the function is actually implemented. High Test case coverage.ess Redundancy. Gaps are covered			
21-)	test cases, compounded by the possibility of gaps of untested software	Trion alo machiom with some sasted to st	[6]	CO1	L3
	Write and explain the improved version of Triangle problem with generated test cases using Normal Boundary value analysis Program: 3Marks Test cases: 2 marks Explanation: 1 Mark				L3

```
Program triangle3'
Dim a, b, c As Integer
Dim c1, c2, c3, IsATriangle As Boolean
'Step 1: Get Input
   Output("Enter 3 integers which are sides of a triangle")
   Input(a, b, c)
   c1 = (1 \le a) \text{ AND } (a \le 300)
   c2 = (1 \le b) AND (b \le 300)
   c3 = (1 \le c) AND (c \le 300)
   If NOT(c1)
        Then Output("Value of a is not in the range of permitted values")
   EndIf
   If NOT(c2)
        Then Output ("Value of b is not in the range of permitted values")
   EndIf
   If NOT(c3)
        ThenOutput("Value of c is not in the range of permitted values")
   EndIf
Until c1 AND c2 AND c3
Output ("Side A is", a)
Output ("Side B is",b)
Output ("Side C is",c)
'Step 2: Is A Triangle?
If (a < b + c) AND (b < a + c) AND (c < a + b)
   Then IsATriangle = True
   Else IsATriangle = False
EndIf
'Step 3: Determine Triangle Type
If IsATriangle
    Then If (a = b) AND (b = c)
              Then Output ("Equilateral")
              Else If (a \neq b) AND (a \neq c) AND (b \neq c)
                        Then Output ("Scalene")
                        Else Output ("Isosceles")
                    EndIf
          EndIf
    Else Output ("Not a Triangle")
EndIf
End triangle3
```

Case	a	b	C	Expected Output		
1	100	100	1	Isosceles		
2	100	100	2	Isosceles		
3	100	100	100	Equilateral		
4	100	100	199	Isosceles		
5	100	100	200	Not a triangle		
6	100	1	100	Isosceles		
7	100	2	100	Isosceles		
8	100	100	100	Equilateral		
9	100	199	100	Isosceles		
10	100	200	100	Not a triangle		
11	1	100	100	Isosceles		
12	2	100	100	Isosceles		
13	100	100	100	Equilateral		
14	199	100	100	Isosceles		
15	200	100	100	Not a triangle		
iagram	t and Debug C : 3marks ion: 3 marl		eat diagram.		[6]	CO1



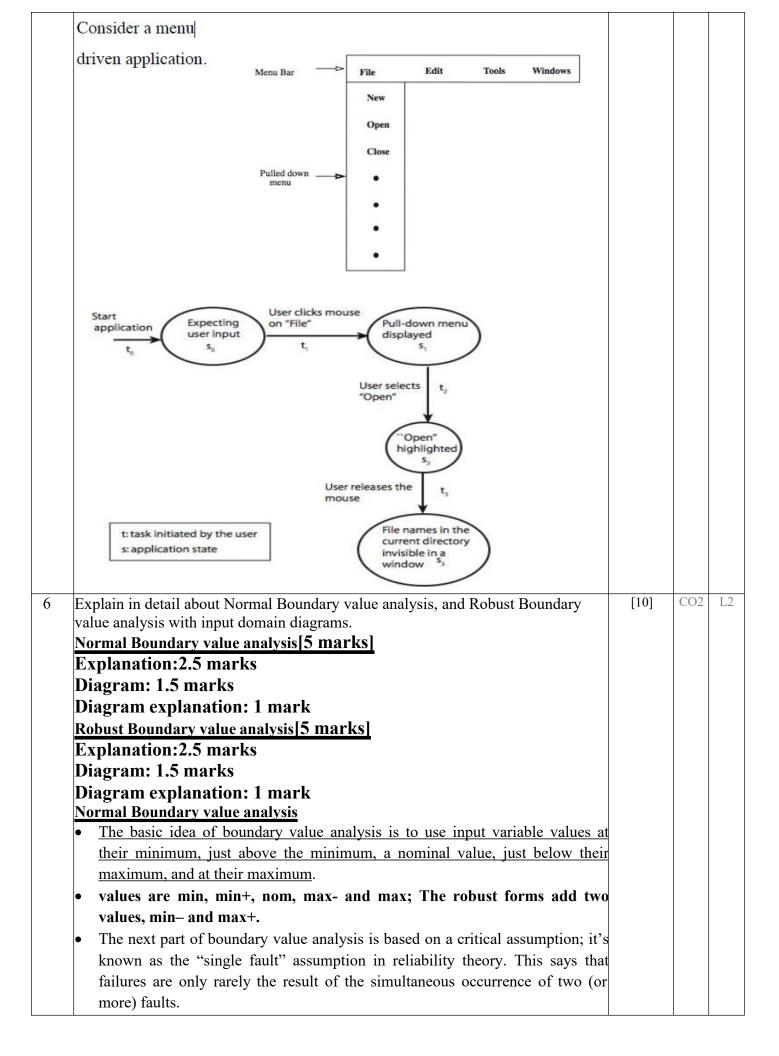
5	1	17500	Invalid Input
6	2	17500	17500
7	17500	34999	34999
8	17500	35000	35000
9	17500	17500	17500
10	34999	17500	34999
11	35000	17500	35000
12	35001	17500	35001
13	17500	17500	17500

Worst case Boundary Value analysis {min, min+, nom, max-, max}={1,2, 17500, 34999,35000} Number of Test cases:= 5ⁿ =5*5=25

If minimum 10 test cases

S.No	a	b	Output
1	1	1	Invalid Input
2	1	2	17500
3	1	17500	17500
4	1	34999	Invalid Input
5	1	35000	Invalid Input
6	2	1	17500
7	2	2	34999
8	2	17500	35000
9	2	34999	17500
10	2	35000	34999
11	17500	1	35000
12	17500	2	35001
13	17500	17500	17500
14	17500	34999	Invalid Input
15	17500	35000	17500
16	34999	1	17500
17	34999	2	Invalid Input
18	34999	17500	Invalid Input
19	34999	34999	17500
20	34999	35000	34999
21	35000	1	35000
22	35000	2	17500
23	35000	17500	34999
24	35000	34999	35000
25	35000	35000	35001

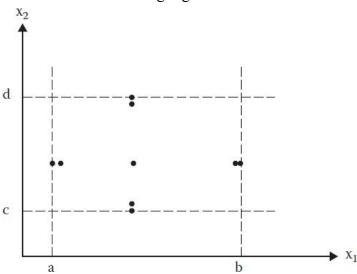
	5.53	001	T 0
5 Explain Test generation strategies with diagram	[5]	CO1	L2
 The tests are generated using a mix of formal and informal methods either directly from the requirements document serving as the source. In more advanced test processes, requirements serve as a source for the development of formal models. 			
 Several strategies are there for test case generation These techniques identify input variables and use formal techniques for test generation and cause effect graphing. 			
 Another way is use of model based testing They need subset of requirements to be modeled using a formal notation which is called as specification. The tests are generated from specification using FSMs Statecharts, Petri Nets and Timed I/O Automata notations for modeling. 			
 Unified modeling language can also used for modeling the requirements into proper specification for test case generation. Model can also be built using predicate Logic and algebraic languages. Each model has its own strengths and weaknesses 			
Requirements Test generation algorithm Test generation			
Finite state machines algorithm State charts Test generation algorithm			
Petri nets Test generation algorithm			
Timed I/O Automata Test generation algorithm Algebraic and logic Test generation			
specifications algorithm Test generation			
Run-time data Test cases			
 Code based techniques can be used to generate tests, or modify existing ones, to generate new tests that force a condition to evaluate to true or false. Two techniques: Program mutation and control flow coverage techniques 			
(b) Explain how to write a Oracle program for GUI with example. Draw the state diagram for the same. Example with diagram 2 marks State Diagram: 2 marks	[5]	CO1	L3
State Diagram. 2 marks			



- Thus the boundary value analysis test cases are obtained by holding the values of all but one variable at their nominal values, and letting that variable assume its extreme values.
- The boundary value analysis test cases for our function F of two variables are:

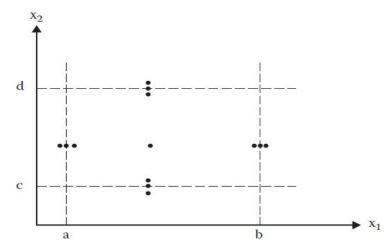
{<x1nom, x2min>, <x1nom, x2min+ >,<x1nom, x2nom>,<x1nom, x2max- >, <x1nom, x2max>, <x1min, x2nom >, <x1min+, x2nom >, <x1max-, x2nom >, <x1max, x2nom > }

• These are illustrated in the following Figure.



Robust Boundary value analysis

- Robust boundary value testing is a simple extension of normal boundary value testing: in addition to the five boundary value analysis values of a variable, we see what happens when the extrema are exceeded with a value slightly greater than the maximum (max+) and a value slightly less than the minimum (min-).
- Robustness test cases for our continuing example are shown in Figure.



- Most of the discussion of boundary value analysis applies directly to robustness testing, especially the generalizations and limitations. The most interesting part of robustness testing is not with the inputs but with the expected outputs.
- The main value of robustness testing is that it forces attention on exception handling. With strongly typed languages, robustness testing may be very awkward.
- Pascal, for example, if a variable is defined to be within a certain range, values

outside that range result in run-time errors that abort normal execution.
This raises an interesting question of implementation philosophy: is it better to perform explicit range checking and use exception handling to deal with "robust values," or is it better to stay with strong typing? The exception handling choice mandates robustness testing.