$\frac{\text{CMR}}{\text{N} \text{STITUTE}}$  OF TECHNOLOGY

**USN** 

 $\widetilde{\mathcal{R}}$ 

 $\sigma_{\rm 0.0}$ See) CMPIT

Internal Assesment Test - I



 $P.T.O$ 



HOD



**Non-inverting Summing Amplifier** 

Therefore, using the superposition theorem, the voltage  $V_2 = V_1$  $V_b \& V_c = 0$ . Net resistance  $=\mathbf{\tilde{R}}+\mathbf{R}\tilde{Z}$ 

$$
V_1 = \frac{R/2}{R + R/2} \tilde{V}_a + \frac{R/2}{R + R/2} V_b + \frac{R/2}{R + R/2} V_c
$$
  
\n
$$
V_1 = \frac{\frac{R/2}{3R/2} V_a + \frac{R/2}{3R/2} V_b + \frac{R/2}{3R/2} V_c}{3}
$$
  
\n
$$
V_1 = \frac{V_a}{3} + \frac{V_b}{3} + \frac{V_c}{3} = \frac{V_a + V_b + V_c}{3}
$$
  
\nIf R<sub>p</sub>=2R<sub>p</sub>, 1+R<sub>p</sub>/R<sub>p</sub>=3  
\n
$$
V_a = V_a + V_b + V_c
$$

2.

**Input bias current**:The dc current required by the inputs of the amplifier to properly operate the first stage.It Is the average of both input currents.

**Input Offset Current**It is the difference of input bias currents.

**Slew Rate**It is the maximum rate of change of the output voltage in response to a step input voltage.

COMMON-MODE REJECTION RATIO (CMRR) The ability of amplifier to reject the common-mode signals (unwanted signals) while amplifying the differential signal (desired signal).

**The input offset voltage** is defined as **the voltage that must be applied between the two input terminals of the op amp to obtain zero volts at the output**. Ideally the output of the op amp should be at zero volts when the inputs are grounded. In reality the input terminals are at slightly different dc potentials.



Output voltage Vab of the bridge is applied to differential instrumentation amplifier composed to three op amp. Gain of the basic amplifier is (-RF/R1), then Vo is

Voltage Vab across the output terminal

 $\Delta$ R is small and  $2R + \Delta R = 2R$ 

$$
\mathsf{Vo} = \mathsf{Vab} \cdot \frac{\mathsf{RF}}{\mathsf{R1}} \qquad = -\frac{\Delta \mathsf{R}(\mathsf{Vdc})}{2(2\mathsf{R} + \Delta \mathsf{R})} \frac{\mathsf{RF}}{\mathsf{R1}} \qquad \qquad \mathsf{Vo} = \frac{\mathsf{RF}}{\mathsf{R1}} \frac{\Delta \mathsf{R}}{4\mathsf{R}} \mathsf{Vdc}
$$

4.



## **First Order Low Pass Filter**

The resistors  $R_f$  and  $R_1$  decide the gain of the filter in the pass band.

The impedance of the capacitor C is  $-jX_C$  where  $X_C$  is the capacitive reactance given by  $X_C = 1/2\pi fC$ .

$$
V_A = -\frac{-jXC}{R - jXC} * VIN
$$

$$
V_A = -\frac{-j\frac{1}{2\pi fC}}{R - j\frac{1}{2\pi fC}} * VIN = \frac{V_{IN}}{1 - \frac{2\pi fRC}{j}}
$$

$$
-j = 1/j; \quad j = -1/j
$$

R<br>Ww  $W_{\text{DE}}$ 

First order low pass butterworth filter

 $V_A = -\frac{V_{IN}}{1 + j2\pi fRC}$ 



## VTU: Aug.-02, July-08,09, Jan.-16, Marks 6

Solution: As decay rate in the stop band is  $+40$  dB/decade, it is second order high

Choose  $C_2 = C_3 = C = 1000 pF$ <br> $f_L = 6 kHz$ and  $f_{\rm L} = \frac{1}{2\pi RC}$ Λ i.e.  $6 \times 10^3$  =  $2\pi R\times1000\times10^{-12}$  $R = 26.525 k\Omega \approx 27 k\Omega$ Α,  $\mathcal{L}_{\mathbf{z}}$  $R = R_2 = R_3 = 27 k\Omega$ 

For Butterworth response,

$$
A_F = 1.586 = \frac{R_f}{R_1} + 1
$$

A.  $R_f = 0.586 R_1$ 

 $R_f = 10 \text{ k}\Omega$  and  $R_1 = 17 \text{ k}\Omega$ Choose

Hence the designed circuit is,



6.



5.

Let us use the superposition principle to obtain the expression for the output voltage  $V_{\rm O}$ 

Assume input to the non-inverting terminal zero. The circuit acts as an inverting amplifier.

$$
V_{O1} = -\frac{R_f}{R_1} V_{in}
$$

 $\ddot{\cdot}$ 

 $V_{O1} = -V_{in}$ as  $R_f = R_1$ **AND REAL** 

 $\dots$  (2.12.1)

Now, assume input to the inverting terminal zero. The circuit acts as a non-inverting amplifier.

$$
V_{O2} = \left(1 + \frac{R_f}{R_1}\right) V_A
$$
  
\n
$$
V_{O2} = 2 V_A \text{ as } R_f = R_1
$$
  
\n
$$
V_A = \text{Voltage at node A}
$$
 ... (2.12.2)

 $_{\rm{By}}$  the potential divider rule, the voltage  $\rm{V_A}$  can be obtained as

$$
V_{A} = V_{in} \left[ \frac{-j X_{C}}{R - j X_{C}} \right]
$$
  
where  $-j X_{C} = -j \left( \frac{1}{2 \pi f C} \right) = \left( \frac{1}{j 2 \pi f C} \right)$  as  $-j = \frac{1}{j}$   
 $\therefore$   $V_{A} = V_{in} \left[ \frac{\frac{1}{j 2 \pi f C}}{R + \frac{1}{j 2 \pi f C}} \right] = V_{in} \left[ \frac{1}{1 + j 2 \pi f RC} \right]$  ... (2.12.3)

Substituting in (2.12.2),

÷.

ż.

$$
V_{O2} = 2 V_{in} \left[ \frac{1}{1 + j 2 \pi f RC} \right]
$$
...(2.12.4)

Hence, the total output voltage is

$$
V_{O} = V_{O1} + V_{O2} = -V_{in} + 2 V_{in} \left[ \frac{1}{1 + j \, 2 \, \pi \, fRC} \right]
$$
  
\n
$$
V_{O} = V_{in} \left[ -1 + \frac{2}{1 + j \, 2 \, \pi \, fRC} \right]
$$
  
\n... (2.12.5)  
\n
$$
\frac{V_{O}}{V_{in}} = \frac{1 - j \, 2 \, \pi \, fRC}{1 + j \, 2 \, \pi \, fRC}
$$
  
\n... (2.12.6)

The magnitude of the transfer function is

$$
\left|\frac{V_O}{V_{in}}\right| = \frac{\sqrt{1 + (2 \pi f R C)^2}}{\sqrt{1 + (2 \pi f R C)^2}} = 1
$$
 ... (2.12.7)

It is mentioned earlier that the magnitude is always 1 for all pass filter and it can pass the entire range of frequency. But the phase angle is given by

$$
\phi = -2 \tan^{-1} \left( \frac{2 \pi f R C}{1} \right) \dots (2.12.8)
$$