
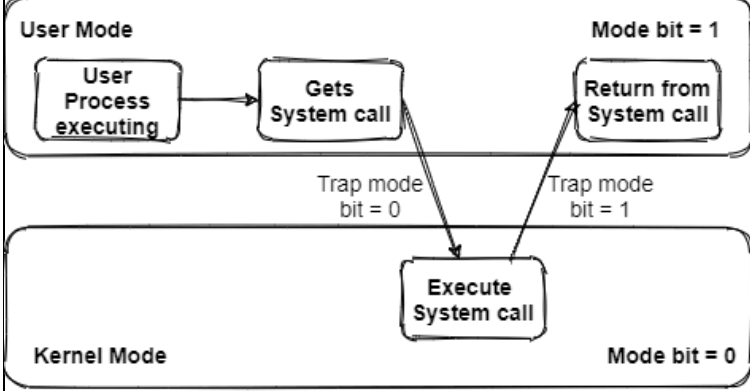
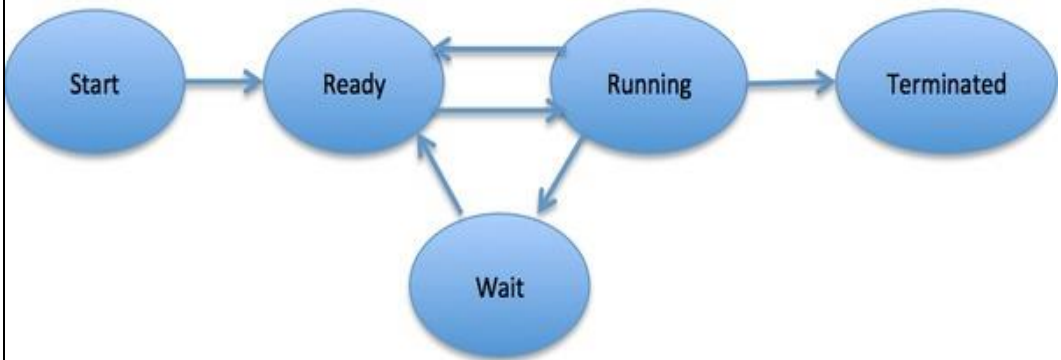


Semester End Examination – July/August 2022
Scheme and Solution
Faculty-Divya Singh/Rashmi D

Sub:	Operating System					Sub Code:	18CS43	Branch:	ISE																									
Date:	14/10/2022	Duration:	3hrs	Max Marks:	100	Sem/Sec:	IV A, B & C																											
									MARKS																									
1	<p>Distinguish between – MULTIPROGRAMMING AND MULTITASKING</p> <table border="1" style="width: 100%; border-collapse: collapse; margin: 10px 0;"> <thead> <tr style="background-color: #e0f2f1;"> <th style="text-align: left;">Parameter</th> <th style="text-align: left;">Multiprogramming</th> <th style="text-align: left;">Multitasking</th> </tr> </thead> <tbody> <tr> <td>Definition</td> <td>It lets multiple programs use the CPU at the same time.</td> <td>Multitasking refers to the simultaneous execution of numerous programmes, processes, and threads with the certain timestamp.</td> </tr> <tr> <td>Objective</td> <td>It's useful for cutting down on CPU idle time and boosting throughput as much as possible.</td> <td>It may be used to execute numerous tasks at once, significantly improving CPU and system throughput.</td> </tr> <tr> <td>Mechanism</td> <td>The context switching method is used.</td> <td>Based on a time-sharing mechanism.</td> </tr> <tr> <td>Time</td> <td>Multiprogramming demands comparatively more time to complete tasks.</td> <td>Multitasking allows you to complete tasks in less time.</td> </tr> <tr> <td>Execution</td> <td>In a multi-programmed system, when one job or process completes its execution or changes to an I/O task, the system temporarily suspends that process. It chooses a new process to execute from the process scheduling pool.</td> <td>Multiple processes can run concurrently in this system by assigning the CPU for a fixed duration of time.</td> </tr> <tr> <td>CPU Switching</td> <td>The CPU shifts between processes swiftly in this environment.</td> <td>The CPU shifts between the processes of several programs in a single-user environment.</td> </tr> <tr> <td>CPU required</td> <td>Only one CPU is needed in Multiprogramming to run the tasks.</td> <td>Multiple CPUs are required for the task allocation.</td> </tr> </tbody> </table> <p style="text-align: center; font-size: small;">Multiprogramming vs. Multitasking</p> <p>Multiprocessor systems and Clustered systems Multiprocessor systems are single computers which have multiple CPUs. Clusters are groups of computers which work together to accomplish some specific work</p>									Parameter	Multiprogramming	Multitasking	Definition	It lets multiple programs use the CPU at the same time.	Multitasking refers to the simultaneous execution of numerous programmes, processes, and threads with the certain timestamp.	Objective	It's useful for cutting down on CPU idle time and boosting throughput as much as possible.	It may be used to execute numerous tasks at once, significantly improving CPU and system throughput.	Mechanism	The context switching method is used.	Based on a time-sharing mechanism.	Time	Multiprogramming demands comparatively more time to complete tasks.	Multitasking allows you to complete tasks in less time.	Execution	In a multi-programmed system, when one job or process completes its execution or changes to an I/O task, the system temporarily suspends that process. It chooses a new process to execute from the process scheduling pool.	Multiple processes can run concurrently in this system by assigning the CPU for a fixed duration of time.	CPU Switching	The CPU shifts between processes swiftly in this environment.	The CPU shifts between the processes of several programs in a single-user environment.	CPU required	Only one CPU is needed in Multiprogramming to run the tasks.	Multiple CPUs are required for the task allocation.	
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1b	<p>What is an operating system and explain dual-mode OS?</p> <p>An operating system (OS) is a program that, after being initially loaded into the computer by a boot program, manages all of the other application programs in a computer. The application programs make use of the operating system by making requests for services through a defined application program interface (API). In addition, users can interact directly with the operating system through a user interface, such as a command-line interface (CLI) or a graphical UI (GUI).</p> <p>Dual Mode OS</p>  <pre> graph TD subgraph USER_MODE [USER MODE] UA[User Application] libc[libc] end subgraph KERNEL_MODE [KERNEL MODE] SCI[System Call Interface (SCI)] K[Kernel] H[Hardware] end UA --> libc libc -- "Transition from User Mode to Kernel Mode using Software Interrupt Instruction (INT for x86)" --> SCI SCI --> K K --> H </pre>																																	
	<p>c) Explain about system calls with an example of handling a user application invoking the open () system call.</p>																																	



2a OR
 2 a. What is a process? Illustrate with a neat diagram the different states of a process and control block. (05 Marks)
 A process is defined as an entity which represents the basic unit of work to be implemented in the system.



2b Discuss the implementation of IPC using message passing systems in detail.
 Inter-process communication is used for exchanging useful information between numerous threads in one or more processes (or programs)."

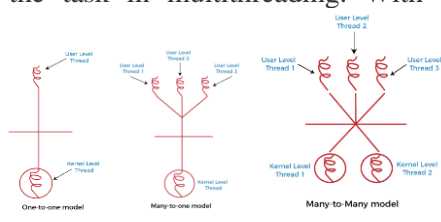
2c List and explain the services provided by OS for the user and efficient operation of system. (05 Marks)
 An Operating System provides services to both the users and to the programs.

- It provides programs an environment to execute.
- It provides users the services to execute the programs in a convenient manner.

Following are a few common services provided by an operating system –

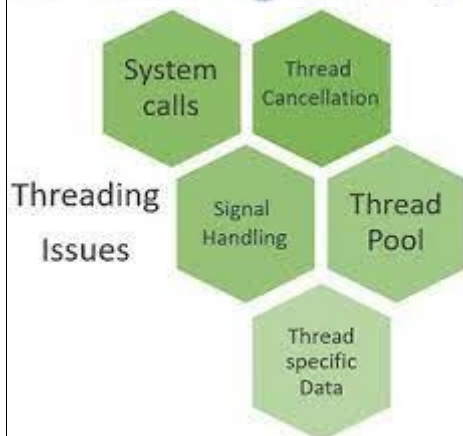
- Program execution
- I/O operations
- File System manipulation
- Communication
- Error Detection
- Resource Allocation
- Protection

3a Give a brief description about multithreading and explain the different multi threading models. (05 Marks)
 Multithreading allows the application to divide its task into individual threads. In multi-threads, the same process or task can be done by the number of threads, or we can say that there is more than one thread to perform the task in multithreading. With the use of multithreading, multitasking can be achieved.



3b

Discuss the issues that come with multithreaded programming.



3c

Explain CPU scheduling criteria.

The scheduling criterion is responsible for helping in the design of the good scheduler. These criteria are as follows –

CPU Utilization

The scheduling algorithm should be designed in such a way that the usage of the CPU should be as efficient as possible.

Throughput

It can be defined as the number of processes executed by the CPU in a given amount of time. It is used to find the efficiency of a CPU.

Response Time

The Response time is the time taken to start the job when the job enters the queues so that the scheduler should be able to minimize the response time.

Response time = Time at which the process gets the CPU for the first time - Arrival time

Turnaround time

Turnaround time is the total amount of time spent by the process from coming in the ready state for the first time to its completion.

Turnaround time = Burst time + Waiting time

or

Turnaround time = Exit time - Arrival time

Waiting time

The Waiting time is nothing but where there are many jobs that are competing for the execution so the Waiting time should be minimized.

Waiting time = Turnaround time - Burst time

Fairness

For schedulers, there should be fairness in making sure that the processes get a fair share of chances to be executed.

4a

4 a. Calculate the average waiting time and the average turnaround time by drawing the Gantt chart using FCFS, SRTF, RR ($q = 2\text{ms}$) and priority algorithms. Lower priority number represents higher priority.

Process	Arrival Time	Burst Time	Priority
P ₁	0	9	3
P ₂	1	4	2
P ₃	2	9	1
P ₄	3	5	4

(12 Marks)

PRIORITY SCHEDULING

	P1	P2	P3	P4
2	11	15	24	29

	AT	BT	TAT	WT
P1	0	24	24	15
P2	1	15	14	10
P3	2	11	9	0
P4	3	29	26	21

$$\text{Avg WT} = \frac{(15+10+0+21)}{4} = 11.5$$

$$\text{Avg TAT} = \frac{(24+14+9+26)}{4} = 18.25$$

SRTF

FCFS

	P1	P2	P3	P4
0	9	13	22	27

	AT	BT	CT	TAT	WT
P1	0	9	9	9	0
P2	1	4	13	12	8
P3	2	9	22	20	19
P4	3	5	27	24	19

$$\text{Avg TAT} = \frac{9+12+20+24}{4} = 16.25$$

$$\text{Avg WT} = \frac{0+8+19+19}{4} = 11.5$$

SRTF

	P1	P2	P4	P1	P3
0					
1					
5					
10					
18					
27					

	AT	CT	TAT	WT
P1	0	18	18	18
P2	1	5	4	3
P3	2	27	25	23
P4	3	10	7	4

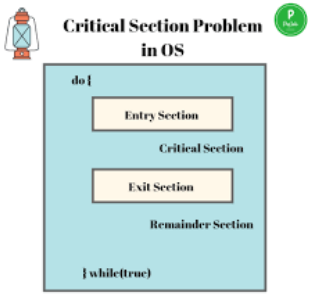
$$\frac{(18+4+25+7)}{4} = 13.5$$

$$\frac{(18+3+23+4)}{4} = 12$$

4b

b. What is critical section problem? What are the requirements for the solution to critical section problem? Explain Peterson's solution. (08 Marks)

The critical section problem is used to design a protocol followed by a group of processes, so that when one process has entered its critical section, no other process is allowed to execute in its critical section.



Peterson's algorithm (or Peterson's solution) is a concurrent programming algorithm for mutual exclusion that allows two or more processes to share a single-use resource without conflict, using only shared memory for communication. It was formulated by Gary L. Peterson in 1981.

Peterson's solution

```

turn = 0;
flag[0] = false;
flag[1] = false;

P0
while(true){
  flag[0] = true;
  turn = 1;
  while (flag[1] and turn ==1) do no-op;
  CS
  flag[0] = false;
  remainder section;
}

P1
while(true){
  flag[1] = true;
  turn = 0;
  while (flag[0] and turn ==0) do no-op;
  CS
  flag[1] = false;
  remainder section;
}

```

5a **What is a deadlock? What are the necessary conditions for the deadlock to occur? (**

Deadlock is a situation where two or more processes are waiting for each other. For example, let us assume, we have two processes P1 and P2. Now, process P1 is holding the resource R1 and is waiting for the resource R2. At the same time, the process P2 is having the resource R2 and is waiting for the resource R1. So, the process P1 is waiting for process P2 to release its resource and at the same time, the process P2 is waiting for process P1 to release its resource. And no one is releasing any resource. So, both are waiting for each other to release the resource. This leads to infinite waiting and no work is done here. This is called Deadlock.

Necessary conditions-

1. Mutual exclusion
2. No pre-emption
3. Hold and wait
4. Circular wait

5b **How to prevent the occurrence of deadlock, explain in detail.**

Banker's algorithms

5c Consider the following snapshot of a system:

Process	Allocation				Max				Available			
	A	B	C	D	A	B	C	D	A	B	C	D
P ₀	2	0	0	1	4	2	1	2	3	3	2	1
P ₁	3	1	2	1	5	2	5	2				
P ₂	2	1	0	3	2	3	1	6				
P ₃	1	3	1	2	4	2	4					
P ₄	1	4	3	2	3	6	6	5				

Answer the following using Banker's algorithm.

- Is the system in safe state? If so, give the safe sequence.
- If process P₂ requests (0, 1, 1, 3) resources can it be granted immediately? (

The Banker's Algorithm

Handles multiple instances for resource types.


n = number of processes; m = number of resource types;

Data Structures:

- **Available:** vector [1..m]. Available[j] - the number of instances currently available for resource j .
- **Max:** matrix[1..n, 1..m]. Max[i, j] - the maximum number of instances of resource j that process i can request at any one time.
- **Allocation:** matrix[1..n, 1..m] - process i currently holds Allocation[i, j] instances of resource j .
- **Need:** matrix[1..n, 1..m] - process i may need additional Need[i, j] instances of resource j .

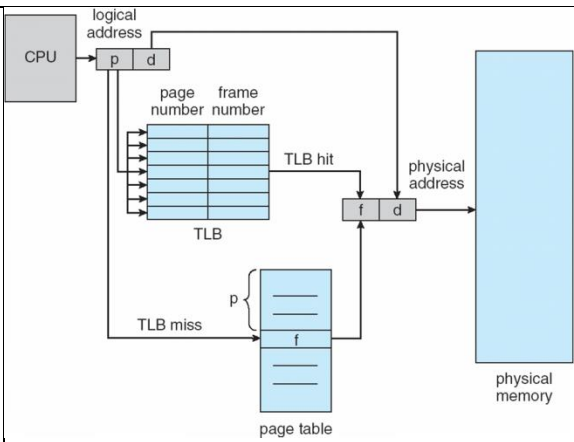
$Need[i, j] = Max[i, j] - Allocation[i, j]$

Fair Amir Fall 00/ Lecture 4



6a. Explain paging hardware with TLB

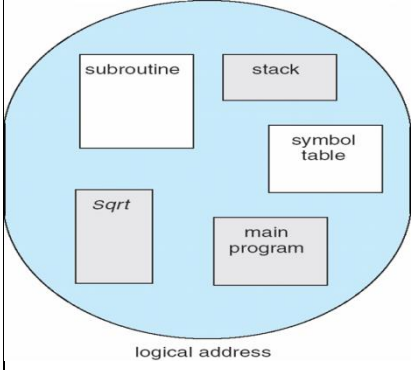
5m



paging hardware with TLB. Explanation of figure given.

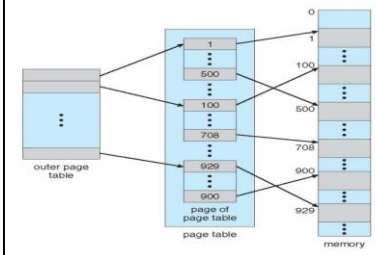
6b Explain segmentation in detail.

5m



6c Discuss structure of page table with suitable diagrams

10m



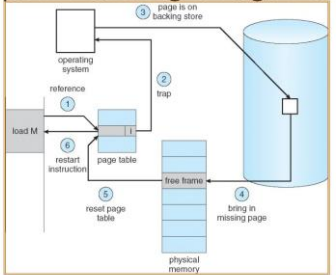
structure of page table and brief about page table

Module 4

7a Describe the steps in handling page faults

6m

Steps in Handling a Page Fault



Brief each step with the diagram given above.

7b Consider the page reference string for a memory with 3 frames. Determine the number of page faults using FIFO, optimal and LRU replacement algorithms.

5

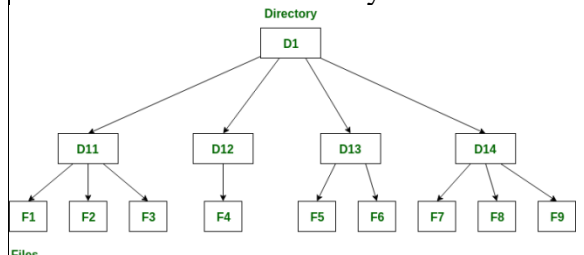
FIFO	FIRST IN FIRST OUT																			
PAGE DEMAND	6	2	3	4	2	1	5	6	2	1	2	3	7	6	3	4	1	2	7	6
f1	6	6	6	6	6	1	1	1	1	1	1	3	3	3	3	3	3	2	2	2
f2	2	2	2	2	2	2	5	5	5	5	5	7	7	7	7	7	7	7	7	6
f3			3	3	3	3	3	6	6	6	6	6	6	6	6	6	4	4	4	4
f4			4	4	4	4	4	4	2	2	2	2	2	2	2	2	2	1	1	1
	fault	fault	fault	fault	hit	fault	fault	fault	fault	hit	hit	fault	fault	hit	hit	fault	fault	fault	hit	fault
REFERENCE STRING	20																			
PAGE FAULTS	14																			
PAGE HITS																				
OPTIMAL PAGE REPLACEMENT																				
REFERENCE STRING	6 2 3 4 2 1 5 6 2 1 2 3 7 6 3 4 1 2 7 6																			
f1	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6
f2	2	2	2	2	2	2	2	2	2	2	2	2	7	7	7	7	7	7	7	7
f3			3	3	3	3	5	5	5	5	5	5	3	3	3	3	3	4	4	2
f4			4	4	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	fault	fault	fault	fault	hit	fault	fault	hit	hit	hit	hit	fault	fault	hit	hit	fault	hit	fault	hit	hit
PAGE FAULT	10																			
PAGE HIT	10																			
LRU LEAST RECENTLY USED																				
REFERENCE STRING	6 2 3 4 2 1 5 6 2 1 2 3 7 6 3 4 1 2 7 6																			
f1	6	6	6	6	6	1	1	1	1	1	1	1	1	6	6	6	6	2	2	2
f2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	4	4	4	4
f3			3	3	3	3	5	5	5	5	5	5	3	3	3	3	3	3	3	7
f4			4	4	4	4	4	6	6	6	6	6	7	7	7	7	7	1	1	1
	fault	fault	fault	fault	hit	fault	fault	fault	hit	hit	hit	fault	fault	fault	hit	fault	fault	fault	fault	fault
PAGE FAULT	15																			
PAGE HIT	5																			

8a Explain the different allocation methods
 Contiguous Allocation
 Linked Allocation
 Indexed Allocation

10

8b Discuss the various directory structures with required diagrams.

10



Files
 Directory structures explanation

Module 5

9a Explain the access matrix method of system protection with domain as objects and its implementation

5+5

object \ domain	F ₁	F ₂	F ₃	laser printer	D ₁	D ₂	D ₃	D ₄
D ₁	read		read			switch		
D ₂				print			switch	switch
D ₃		read	execute					
D ₄	read write		read write		switch			

Access matrix explanation along with the matrix given.

9b A drive has 5000 cylinders, numbered 0 to 4999. The drive is currently Serving a request at cylinder 143, and the previous request was at cylinder 125. The queue of pending requests in FIFO order is 86, 1470, 913, 1774, 948, 1509, 1022, 1750, 130. Starting from current head position, what is a total distance (in cylinders) that the Disk arm move to satisfy all a pending requests using FCFS, SSTF, LOOK AND C-LOOK Algorithms?
 FCFS

10m

The FCFS algorithm just follows the order of the requests given.

The FCFS schedule is:

143, 86, 1470, 913, 1774, 948, 1509, 1022, 1750, 130

The total distance is: 7,081

SSTF

The SSTF algorithm starts a cylinder 143 and from there successively selects the shortest request from its current location.

The SSTF schedule is:

143, 130, 86, 913, 948, 1022, 1470, 1509, 1750, 1774

The total distance is: 1,745

LOOK

The LOOK algorithm is just like the SCAN algorithm, except the disk head only goes as far as the last request in each direction.

The LOOK schedule is:

143, 913, 948, 1022, 1470, 1509, 1750, 1774, 130, 86

The total distance is: 3,319

C-LOOK

The C-LOOK algorithm is a circular version of the LOOK algorithm. It doesn't scan on the way back to the beginning of the disk, rather operates in a circular fashion.

The C-LOOK schedule is:

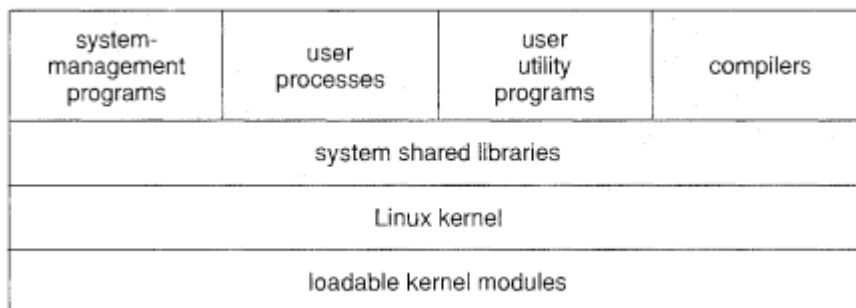
143, 913, 948, 1022, 1470, 1509, 1750, 1774, 86, 130

The total distance is: 3,363

10a

Write a neat diagram, explain the components of a Linux system

8m



Kernel. The kernel is responsible for maintaining all the important abstractions of the operating system, including such things as virtual memory and processes.

System libraries. The system libraries define a standard set of functions through which applications can interact with the kernel. These functions implement much of the operating-system functionality that does not need the full privileges of kernel code.

	System utilities. The system utilities are programs that perform individual, specialized management tasks. Some system utilities may be invoked just once to initialize and configure some aspect of the system; others known as daemons in	
10b	Explain the different IPC mechanisms available in Linux. <ul style="list-style-type: none">• Synchronization and Signals• Passing of Data Among Processes	6m
10c	Discuss about scheduling in Linux <ul style="list-style-type: none">• Process Scheduling• Kernel Synchronization• Symmetric Multiprocessing	6m