

## Fourth Semester B.E./B.Tech. Degree Examination, Dec.2024/Jan.2025 Microcontrollers

Max. Marks: 100

Note: 1. Answer any FIVE full questions, choosing ONE full question from each module.

2. M: Marks, L: Bloom's level, C: Course outcomes.

		Module – 1	M	L	C
Q.1	a.	Bring out the difference between Microprocessor and Microcontroller.	04	L2	COI
	b.	With function of each pin, explain the pin layout of 8051 Microcontroller.	10	L2	CO
	c.	Summarize the internal RAM configuration of 8051.	06	L2	COI
		OR			
Q.2	a.	Differentiate between CISC and RISC processor architectures.	04	L2	CO
	b.	With a neat architecture, explain the architectural features of 8051.	08	L2	CO
	c.	Interface 8051 microcontroller to 16K bytes of EPROM and 8K bytes of RAM. Explain with neat sketch.	08	L3	COI
		Module – 2			
Q.3	a.	What is an addressing mode? Explain 4 different addressing modes of 8051 with examples.	08	L2	CO2
- W	b.	Illustrate with a neat diagram different ranges of jump instructions.	06	L2	CO
	c.	Write an ALP to convert a packed BCD number into two ASCII numbers. Store the result in R5 and R6 respectively.	06	L2	CO2
		OR			
Q.4	a.	Define assembler directives. Explain the same with examples.	08	L2	CO
<b>~··</b>	b.	List and explain bit level logical instructions in 8051.	06	L2	CO
	c.	Develop an assembly language program to swap the contents of R3 and R4 registers in BANKO using different methods.	06	L2	CO2
		Module – 3		57.6	
Q.5	a.	Explain the bit contents of TCON and TMOD registers.	06	L2	CO:
- Zu	b.	Develop an ALP to generate a square wave of frequency 1 kHz on Pin P1.2 using Timer 0 in mode 2. Show the delay calculation. Assume XTAL frequency = 22 MHz.	06	L3	CO
	c.	Explain RS232 in serial communication using 8051 Microcontroller with DB-9 pin connector.	08	L2	CO
		OR			
Q.6	a.	Explain the bit pattern of SCON register with diagram.	04	L2	CO
	b.	Develop an 8051 C program to transfer letter "A" serially at 9600 baud rate, 8 bit data, 1 stop bit, do this continuously.	08	L3	CO:
	c.	Explain Mode 2 operations of timers and explain steps involved in programming timer in Mod 2, with necessary diagram.	08	L2	CO
		Module – 4			
Q.7	a.	Explain the structure of interrupt priority and interrupt enable register.	08	L2	CO
	b.	Explain interrupt vector table of 8051 Microcontroller.	06	L2	CO
	c.	Explain programming of Timer interrupts.	06	L2	CO
		1of 2			

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		OR	04	L2	CO <sub>4</sub>
Q.8	a.	List the steps involved in executing an interrupt.  Write an ALP program using interrupts to generate a square wave on port	08	L3	CO4
	b.	pin P1.2 of 10 kHz using timer 0 in mode 2, XTAL = 22 MHz.			
	c.	Explain the steps involved in programming serial communication	08	L2	CO4
	С.	interrupts.			
		Module - 5	10	L3	COS
Q.9	a.	With a neat diagram, write an 'C' language program to interface DAC to	10	L3	CO.
		8051 Microcontroller to generate staircase waveform with 20 steps.	10	L3	COS
	b.	Explain the interfacing of DC motor using C programming.  CMRIT LIBRARY  CMRIT LIBRARY	10	Lo	
		OR BANGALORE - 560 037			
Q.10	a.	With neat diagram, write an C language program to interface stepper motor	10	L3	CO
Q.10		to 8051 Microcontroller			
	b.	Write a C program to display 'HELLO WORLD' by interfacing LCD	10	L3	CO
		display to 8051 Microcontroller.		100	
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