

BECOMING AN INNOVATOR

BCS SCHEME

USN _____

BEC304

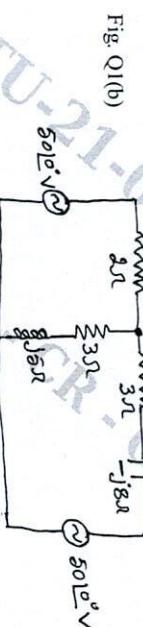
Third Semester B.E./B.Tech. Degree Examination, Dec.2024/Jan.2025

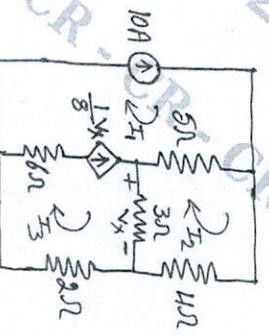
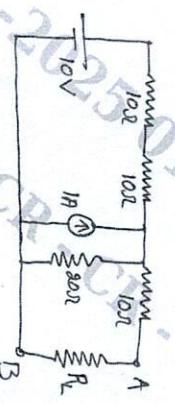
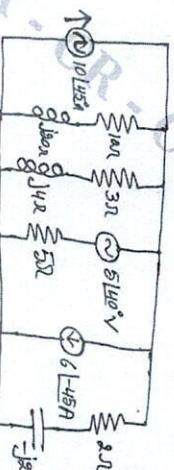
Network Analysis

Time: 3 hrs.

Max. Marks: 100

Note: 1. Answer any FIVE full questions, choosing ONE full question from each module.
2. M : Marks , L. : Bloom's level , C. : Course outcomes.

Module - 1			
	M	L	C
Q.1 a.	Three impedances are connected in Delta. Obtain the star equivalent of the network.	7	L3 CO1
b.	For the circuit shown in Fig. Q1(b). Find the voltage 'V' at node by using nodal analysis.	6	L3 CO1
	Fig. Q1(b)		
			
c.	Determine the current in 12Ω resistor shown in Fig. Q1(c) using source transformation method.	7	L3 CO1
	Fig. Q1(c)		
			

Module - 2			
	M	L	C
Q.2 a.	Find the loop currents I_1 , I_2 , and I_3 in the circuit shown in Fig. Q2(a).	7	L3 CO1
	Fig. Q2(a)		
			
OR			
Q.3 a.	Sate and prove Superposition theorem.	7	L2 CO2
b.	For the circuit shown in Fig. Q3(b), obtain the Thevenin's equivalent circuit.	7	L3 CO2
	Fig. Q3(b)		
			
c.	Using Millman's theorem, find current flowing through $(3 + j4) \Omega$ impedance for the circuit shown in Fig. Q3(c).	6	L3 CO2
	Fig. Q3(c)		
			
OR			

Q.4	a.	State and prove Norton's theorem.					7	L2	CO2
	b.	Find the value of Z_L for Maximum Power transfer and the value of Maximum power for the circuit shown in Fig. Q4(b)					6	L3	CO2
	c.	Find current 'I' using Super position theorem for the circuit shown in Fig. Q4(c).					7	L3	CO2

Fig. Q4(b)

Fig. Q4(c)

Module - 3

- Q.5 a. Use the concepts of initial condition to illustrate the voltage behavior in inductor circuit for DC supply.

- b. In the circuit steady state is reached with switch 'K' open. The switch is closed at $t = 0$. Compute i , di/dt and d^2i/dt^2 at $t = 0^+$.

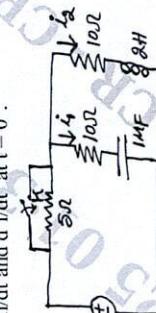


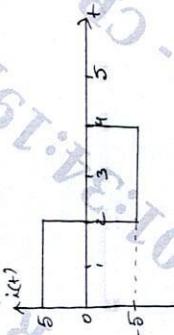
Fig. Q5(b)

- Q.6 a. In the circuit shown in Fig. Q6(a), determine complete solution for current when switch 'K' is closed at $t = 0$.



Fig. Q6(a)

- b. Compute v , dv/dt , d^2v/dt^2 at $t = 0^+$ for the circuit shown in below Fig. Q6(b), when the switch K is opened at $t = 0$.

**Module - 4**

- Q.7 a. Using waveform synthesis method to express the voltage pulse terms of unit step. Find i) $L\{i(t)\}$ ii) $L\{\int i(t).dt\}$.

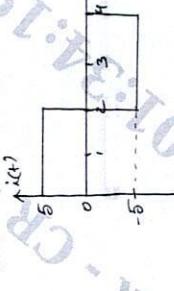


Fig. Q7(a)

- b. State and prove initial value and final value theorem for Laplace transform.



Fig. Q7(b)

- c. Obtain the Laplace transform of step and ramp function with relevant expressions.

OR

- Q.8 a. Determine $i_L(t)$ for $t \geq 0$ using Laplace transform for circuit shown in Fig. Q8(a).

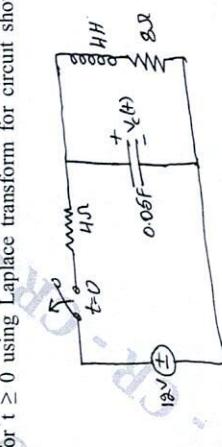


Fig. Q8(a)

- b. Determine $i_L(t)$ for $t \geq 0$ using Laplace transform for circuit shown in Fig. Q8(b).

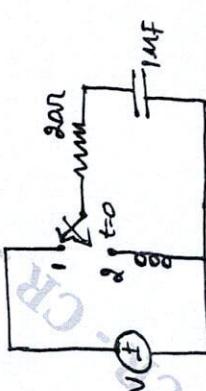
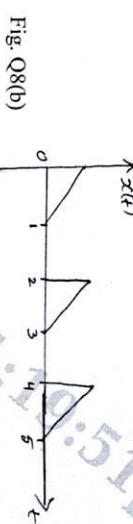


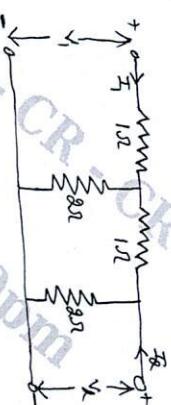
Fig. Q8(b)

- b. Find the Laplace transform of the periodic signal $x(t)$ as shown in Fig. Q8(b).



Module - 5

- Q.9**
- Define Z -parameters. Determine Y -parameters in terms of Z -parameters. **6** L3 CO5
 - Show that resonant frequency is geometric mean of cut off frequency in series $R - L - C$ circuit. **7** L3 CO5
 - Apply the two - port network analysis technique to determine ABCD - parameters of the network shown in Fig. Q9(c). **7** L3 CO5



OR

- Q.10** a. Derive the expression for the resonant frequency of the circuit shown in Fig. Q10(a). Also show that the circuit resonate at all frequency if

$$R_L = R_C = \sqrt{\frac{L}{C}}$$

Fig. Q10(a)

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- b. The model of a transistor in the CE mode is shown in Fig. Q10(b). Determine the h-parameters.

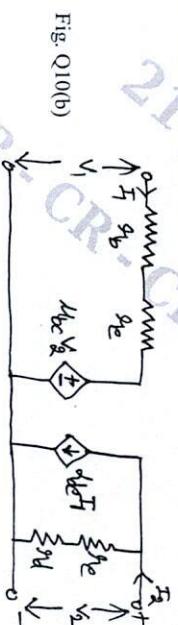


Fig. Q10(b)



Current Session : 09:30:00

09:30:00 Session is in progress.

09:30:00 Session



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Feedback/Remarks

Date:21-01-2025 | Session:09:30:00

For Subject Code : BEC304

1. Q. 5) c) the value of the inductor to be taken as 1H (one henry)

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