CMR

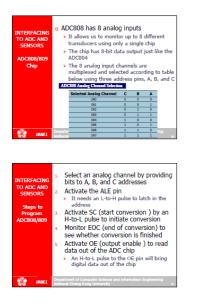
INSTITUTE OF	USN						
TECHNOLOGY							ACCOUNTED WITH AN UNABLE OF TRACE

Sub:	Microcontroller							Code:	15E	E52	
Date:	19/11/2018	Duration:	90 mins	Max Marks:	50	Sem:	5	Branch:	EEE		
			Answe	r Any FIVE FULL Que	estions						
								Ma	rks	OE	BE
								-		СО	RBT
1	Explain the features of	of ADC 0808. A	so explain tl	he working of it	s various	s pins		[1	0]	CO1	L4
2	Explain the bit statu transfer letter 'A' ser	•		•	also a v	write a C	prograr	n to [1	0]	CO1	L4
3	Write a C program to	send 'M', 'A', '	Ś <i>', '</i> T' <i>, '</i> E' <i>, '</i> R	' to LCD display	·.			[1	0]	CO2	L3

4	Illustrate the control byte structure of serial ADC. Write a program for selection of a channel.	[10]	CO2	L3
5	Show the interfacing of a stepper motor to 8051 and write a program to rotate stepper motor 5 steps in clockwise and 10 in anticlockwise direction with a delay between each step.	[10]	CO1	L3
6	List the interrupts in 8051 and explain the steps in executing an interrupt.	[10]	CO1	L1
7	Write an 8051 C program using interrupts to do the following:	[10]	CO2	L3
	i) Receive the data serially and send it to PO.			
	ii) Read port P1, transmit data serially and give a copy to P2.			
	iii) Make timer 0 generate a square wave of 5 KHz frequency on P0.1.			
	Assume that XTAL=11.0592 MHz. Set the baud rate at 4800.			

1. Explain the features of ADC 0808. Also explain the working of its various pins.

## Internal Assesment Test - III





**2.** Explain the bit status of SCON special function register. And also a write a C program to transfer letter 'A' serially at 4800 baud rate continuously.

SERIAL COMMUNICA- TION PROGRAMMING	<ul> <li>SCON is an 8-bit register used to program the start bit, stop bit, and data bits of data framing, among other things</li> </ul>									
SCON Register	SM0 SM1 SM0 SCON.7 SM1 SCON.6 SM1 SCON.6 SM1 SCON.5 REN SCON.4 TB8 SCON.3 RB8 SCON.2 TI SCON.1 RI SCON.0 Note: Make SM	SM2         REN         TB8         RB8         TI         RI           Serial port mode specifier         Used for multiprocessor communication         Sericlared by software to enable/disable reception           Not widely used         Not widely used         Transmit interrupt flag. Set by HW at the begin of the stop bit mode 1. And cleared by SW           Receive interrupt flag. Set by HW at the begin of the stop bit mode 1. And cleared by SW         22, TB8, and RB8 =0								
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Write a program for the 8051 to transfer letter "A" serially at 4800 baud, continuously.						
Solution	ı:					
	MOV	TMOD,#20H	;timer 1,mode 2(auto reload)			
	MOV	TH1,#-6	;4800 baud rate			
	MOV	SCON,#50H	;8-bit, 1 stop, REN enabled			
	SETB	TR1	;start timer l			
AGAIN:	MOV	SBUF,#"A"	;letter "A" to transfer			
HERE:	JNB	TI,HERE	;wait for the last bit			
	CLR	TI	;clear TI for next char			
	SJMP	AGAIN	;keep sending A			

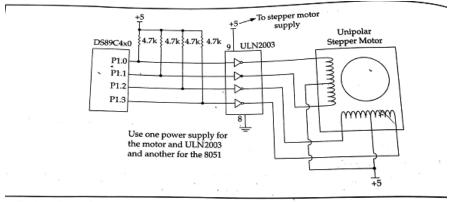
3. Write a C program to send 'M', 'A', 'S', 'T', 'E', 'R' to LCD display.

# include < reg 51. h>		
sh Idata = 0 x90 ;		
sbit is = Paroj		
shit yu: Pari;		
stit en = Pa'a;		
void main ()		
e la companya de la compa		
ledend (0x18);		
MSDoby (250);		
dedand (OXOF);		
MSDelay (250);		
-ledend (oroi);		
MS Delay (250);		
-ledend(0x06);		
MeDelay (250);	void lcdcmd(unsign	ed char value){
-ledand (ox 86);	<pre>lcdready();</pre>	//check the LCD busy flag
MiDelay (250);	-	<pre>//put the value on the pins</pre>
leddata ('M');	rs = 0; rw = 0;	
MS Delay (250):	en = 1;	//strobe the enable pin
leddata ('A');	MSDelay(1);	-
MSDelay (250);	en = 0;	
leddata ('s');	return; }	
Ms Delay (250);		
laddala ('T').	void lcddata(unsig	
1.000		<pre>//check the LCD busy flag //put the value on the pins</pre>
J (30),	rs = 1;	//put the varue on the prins
Loddala ('E');	rw = 0;	
Mr Deby (250);	en = 1;	//strobe the enable pin
Icadata ( 'R');	MSDelay(1);	
MS Deby ( 250);	en = 0;	
2 million and a second	return;	
	,	

4. Illustrate the control byte structure of serial ADC. Write a program for selection of a channel.

Г	Store 1	-	1	SELO	UN/BIP SGL/DF PD1 PD0
L	Start	SEL2	SLE1		
Start	The M	SB (D7)	must be hig	h to define	the beginning of the control byte.
	It mus	t be sent	in first.		TION (SINGLE-ENDED MODE)
SEL2	SEL1	SELO	CHANN	EL SELEC	
0	0	0	CHAN0 CHAN1		
0	1	1	CHAN2		
0	i	1	CHAN3		
1	0	0	CHAN4		
1	0	1	CHAN5		
1	1	0	CHAN6		
1	1	1	CHAN7		
UNI/B	up	1 - 10	inolar Digit	al data out	put is binary 00 - FFH.
SGL/E	DIF				
		0 = dif	ferential: Tw	o channels	(eg., CH0 - CH1) are differential.
PD1		1 6.1	he anaration	1	
		0 = pov	wer-down: P	ower down	n to save power using software. conversion speed is dictated by SCLK.
PD0					
		and	d the SSTRB	pin goes h	igh to indicate end-of-conversion (EOC).
	SETB	CS			deselect ADC, conversion starts
	CLR	SCL	ĸ		;SCLK=0 during conversion
110	Call				;SCLK=0 ddring
#inc	Code 1	or se	ending i	n contr	rol byte for MAX1112 ADC
chit	CS =	P2~0;			//see Figure 13-15
chit	SCLK	= P2^	1;		
abit	DIN =	P2^2	;		
chit	DOUT	= P2^	3;		
soid	MSBRA	= AC	C^7;		
	main(	void)			
{	uncia	nod a	h		
	uneig	ned ci	har x;	oyce=0x	9E; //Chan 1
	ACC=c	onbut	nar x;		
	CS=0;	ombyca	e,		
		-0 · x	<8; x++)		
	{	-0,	(0, 1++)		
		CLK=0			
		IN=MSI			//Send D7 of Reg A to Din
		elay()			//Selid D/ OL Roy P
		CLK=1			//latch in the bit
		elay()			// facen fil bio
			ACC << 1		//next bit
	}				//HEAU DIC
	CS=1;				//deselect MAX1112
	SCLK=0				//Make SCLK low during conversion
}		,			//mance board and and a

5. Show the interfacing of a stepper motor to 8051 and write a program to rotate stepper motor 5 steps in clockwise and 10 in anticlockwise direction with a delay between each step.



re 17-9. 8051 Connection to Stepper Motor

-	
	Stepper Motor
1	(Rotate 5 steps anti-clockwise)
	lo steps anti-clocking)
	· · · · · · · · · · · · · · · · · · ·
í.	Org ococh
	MOV A, # 66h
1	Mov Ro, #osh
21:	RL A
	Acall delay
	DTNZ RO, KI
	Mov A, + 66h
	Mov RI, #OAL
22:	RRA
	Acall delay
	DINZ RI, LJ
delay:	Mov & 2, #FFh
24:	MON R3, #FFh
	DTN2 R3, L3
	DJN2 R2, 24
	Ret
-	End
	C. W.

6. List the interrupts in 8051 and explain the steps in executing an interrupt.

- Six interrupts are allocated as follows
  - Reset power-up reset
  - > Two interrupts are set aside for the timers: one for timer 0 and one for timer 1
  - > Two interrupts are set aside for hardware external interrupts
    - P3.2 and P3.3 are for the external hardware interrupts INTO (or EX1), and INT1 (or EX2)
  - Serial communication has a single interrupt that belongs to both receive and transfer
- The microcontroller gets the address of the ISR from the interrupt vector table and jumps to it
  - It starts to execute the interrupt service subroutine until it reaches the last instruction of the subroutine which is RETI (return from interrupt)
- Upon executing the RETI instruction, the microcontroller returns to the place where it was interrupted
  - First, it gets the program counter (PC) address from the stack by popping the top two bytes of the stack into the PC
  - Then it starts to execute from that address

## Upon activation of an interrupt, the microcontroller goes through the following steps

- It finishes the instruction it is executing and saves the address of the next instruction (PC) on the stack
- It also saves the current status of all the interrupts internally (i.e: not on the stack)
- It jumps to a fixed location in memory, called the interrupt vector table, that holds the address of the ISR

7. Write an 8051 C program using interrupts to do the following:

i) Receive the data serially and send it to PO.ii) Read port P1, transmit data serially and give a copy to P2.

iii) Make timer 0 generate a square wave of 5 KHz frequency on P0.1.

## Assume that XTAL=11.0592 MHz. Set the baud rate at 4800.

				BACK:	SETB SETB	TR1 TR0	0010B ;enable serial int. ;start timer 1 ;start timer 0 ;read data from port 1
	ORG	0					;give a copy to SBUF ;send it to P2
	LJMP	MAIN					;stay in loop indefinitely
			;ISR for timer 0	;		SE	ERIAL PORT ISR
			;toggle P0.1		ORG	100H	
		23H	;return from ISR	SERIAL	:JB	TI, TRANS	3;jump if TI is high
			; jump to serial interrupt ISR		MOV	A, SBUF	;otherwise due to receive
	ORG		, jump to seriar interrapt isk		MOV	PO,A	;send serial data to PO
MAIN:	MOV	P1,#OFE	FH ;make P1 an input port		CLR	RI	;clear RI since CPU doesn't
	MOV	TMOD, #2	22H;timer 1,mode 2(auto reload)		RETI		;return from ISR
	MOV	TH1,#0E	76H;4800 baud rate	TRANS:	CLR	TI	;clear TI since CPU doesn't
	MOV	SCON,#5	50H;8-bit, 1 stop, ren enabled		RETI		;return from ISR
	MOV	тн0,#-9	92 ;for 5kHZ wave		END		,